

A CAMERA SYSTEM HAVING A COMMUNICATION SYSTEM
BETWEEN A CAMERA BODY AND A PHOTOGRAPHING LENS

BACKGROUND OF THE INVENTION

5 1. Field of the Invention

The present invention relates to a camera system having a camera body and a photographing lens which is detachably attached to the camera body, and having a communication system between the camera body and the 10 photographing lens, wherein a rear converter can be mounted between the camera body and the photographing lens.

2. Description of the Related Art

In conventional interchangeable lens cameras, the 15 camera body obtains fundamental information on the photographing lens via predetermined data communication performed between the camera body and the photographing lens. However, if a rear converter is mounted between the camera body and the photographing lens, the data 20 communication cannot be performed, so that it is impossible to use any useful features that the photographing lens may possess.

Even if the rear converter is simply provided with relay channels for connecting each communication channel 25 of the camera body with a corresponding connecting channel

of the photographing lens, the camera body cannot recognize the existence of such rear converter, so that the camera body cannot control operations of the photographing lens properly with only data received from 5 the photographing lens. In addition, if the camera body is provided with additional electrical contacts for data communications between the camera body and the rear converter, the camera body needs to be provided with one or more additional components for the additional 10 electrical contacts, and also switching between the communication channels becomes necessary, complicating 15 the structure of the camera system.

SUMMARY OF THE INVENTION

15 The present invention provides a communication system between a camera body and a photographing lens of a camera system, wherein a rear converter can be mounted between the camera body and the photographing lens, and data on the rear converter can be used without any 20 complicated structure.

For example, in an embodiment, an interchangeable lens camera system having a camera body, a photographing lens, and a rear converter which can be mounted between the camera body and the photographing lens, is provided, 25 the camera body having a first group of contacts, the

photographing lens having a second group of contacts, the camera body and the photographing lens communicating with each other via the first group of contacts and the second group of contacts with the first group of contacts being
5 electrically connected with the second group of contacts, respectively, wherein the rear converter includes a group of relay channels via which the first group of contacts of the camera body are electrically connected with the second group of contacts of the photographing lens,
10 respectively, in a state where the rear converter is mounted between the camera body and the photographing lens; a rear converter memory in which rear converter data on the rear converter is stored, the rear converter memory including at least one port electrically connected to
15 corresponding at least one relay channel of the group of relay channels; and a rear converter controller which controls a reading operation of the rear converter data from the rear converter memory, the rear converter controller including at least one port electrically
20 connected to corresponding at least one relay channel of the group of relay channels. The rear converter memory and the rear converter controller have a function to send the rear converter data to the camera body while the camera body and the photographing lens communicate with each
25 other via the first group of contacts, the second group

of contacts, and the group of relay channels.

It is desirable for the photographing lens to include a lens memory in which photographing lens data is stored; wherein the camera body includes a body controller which communicates with the lens memory to read the photographing lens data from the lens memory; wherein a portion of the photographing lens data serves as dummy data for the rear converter; and wherein the rear converter data is read out of the lens memory to be transmitted to the body controller in synchronization with an operation of the body controller in which the body controller receives the dummy data.

The body controller can be electrically connected to the rear converter controller via a first communication/control contact of each of the first group of contacts and the second group of contacts, and a data I/O contact of each of the first group of contacts and the second group of contacts. The photographing lens can include a lens controller which communicates with the body controller. The body controller is electrically connected to the lens controller via the first communication/control contact, a second communication/control contact of each of the first group of contacts and the second group of contacts, and at least one relay channel of the group of relay channels, wherein

a handshake operation is performed between the body controller and the lens controller via the second communication/control contact. The lens controller sends out dummy data to enable the data I/O contact if
5 inputting a command for the rear converter, which is issued by the body controller, via the data I/O contact, while the lens controller communicates with the body controller. The rear converter sends out the rear converter data to the data I/O contact in the case where
10 the command is input via the data I/O contact.

In an embodiment, the rear converter receives the dummy data from the lens controller in the case where the lens controller receives the command; and the rear converter sends the rear converter data to the body controller in synchronization with an operation of the body controller in which the body controller receives the dummy data.
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The body controller can be set to recognize one of a last one byte and a last few types of the photographing lens data as the dummy data for the rear converter.
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In another embodiment, a rear converter which can be mounted between a camera body and a photographing lens of an interchangeable lens camera system, is provided, the camera body having a first group of contacts, the
25 photographing lens having a second group of contacts, the

camera body and the photographing lens communicating with each other via the first group of contacts and the second group of contacts with the first group of contacts being electrically connected to the second group of contacts,
5 respectively, wherein the rear converter includes a group of relay channels via which the first group of contacts of the camera body are electrically connected with the second group of contacts of the photographing lens, respectively, in a state where the rear converter is
10 mounted between the camera body and the photographing lens; a rear converter memory in which rear converter data is stored, the rear converter memory including ports electrically connected to at least one relay channel of the group of relay channels; and a rear converter
15 controller which controls a reading operation of the rear converter data from the rear converter memory, the rear converter controller including ports electrically connected to at least one relay channel of the group of relay channels. The rear converter memory and the rear converter controller have a function to send the rear converter data to the camera body while the camera body and the photographing lens communicate with each other via the first group of contacts, the second group of contacts, and the group of relay channels.
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25 Each of the first group of contacts and the second

group of contacts can include a first communication/control contact via which the body controller sends a control signal to the lens controller; a second communication/control contact via which the lens controller sends a control signal to the body controller; and a data I/O contact for data communication. The first communication/control contact, the second communication/control contact, and the data I/O contact of the first group of contacts are electrically connected to the first communication/control contact, the second communication/control contact and the data I/O contact of the second group of contacts, respectively, via the group of relay channels. The rear converter memory and the rear converter controller are electrically connected to relay channels of the group of relay channels which correspond to the first communication/control contact and the data I/O contact. The rear converter memory and the rear converter controller have a function to send the rear converter data to the camera body after a commencement of a handshake operation between the body controller and the lens controller via the second communication/control contact in the case where the camera body commands the rear converter controller to send the rear converter data via the data I/O contact.

The present disclosure relates to subject matter

contained in Japanese Patent Application No.2001-54543 (filed on February 28, 2001) which is expressly incorporated herein by reference in its entirety.

BRIEF DESCRIPTION OF THE DRAWINGS

5 The present invention will be described below in detail with reference to the accompanying drawings in which:

10 Figure 1 is a block diagram of fundamental elements of control systems of a camera body and a photographing lens of an SLR camera system having a communication system between the camera body and the photographing lens according to the present invention;

15 Figure 2 is a block diagram of fundamental elements of the control system of the camera body;

20 Figure 3 is a block diagram of fundamental elements of a communication/control system of the photographing lens;

25 Figure 4 is a block diagram of fundamental components of a photographing lens which is provided with a lens controller which operates with a first power, and a peripheral circuit which operates with a second power;

30 Figure 5 is a block diagram of fundamental components of a photographing lens which is provided with a lens controller and a peripheral circuit both of which operate with the first power;

Figures 6A and 6B show a flow charts for the first portion of a main process of the camera body, to which the present invention is applied;

Figure 7 is a flow chart for the remaining portion 5 of the main process of the camera body, to which the present invention is applied;

Figure 8A is a flow chart for a communication identification process including of an old-type communication process and a new-type communication 10 setting request process of the camera body;

Figure 8B is a flow chart for operations of a photographing lens which are performed in accordance with operations of the new-type communication setting request process of the camera body;

15 Figure 9 is a flow chart for the new-type communication setting request process of the camera body;

Figure 10 is a flow chart for a camera state information setting process of the camera body;

20 Figure 11 is a flow chart for an image-shake compensation data setting process of the camera body;

Figure 12A is a block diagram of fundamental elements of a control system of the first embodiment of the photographing lens which incorporates an image-shake compensation device;

25 Figure 12B is a conceptual diagram of a compensation

lens (an image-stabilizing optical system) LC of the image-shake compensation device;

Figure 13 is a flow chart for a main process of the first embodiment of the photographing lens;

5 Figure 14 is a flow chart for a new-type communication setting process of the photographing lens;

Figure 15 is a flow chart for a 1ms-timer interrupt process of the first embodiment of the photographing lens;

10 Figure 16 is a flow chart for the first half of an inverse-INT interrupt process of the first embodiment of the photographing lens;

Figure 17 is a flow chart for the remaining half of the inverse-INT interrupt process of the first embodiment of the photographing lens;

15 Figure 18 is a timing chart for the communication identification process from the moment the main switch of the camera body is turned ON to the moment immediately after the commencement of the new-type communication process;

20 Figure 19A is a timing chart for a handshake operation performed between the camera body and the photographing lens at the commencement of the new-type communication process;

25 Figure 19A is a timing chart for a handshake operation performed between the camera body and the

photographing lens at the commencement of the new-type communication process;

Figure 20 is a timing chart for an old-type communication process that is performed between the
5 camera body and the photographing lens;

Figure 21A is a timing chart for communication in the new-type communication process that is performed between the camera body and the photographing lens;

Figure 21B is a timing chart for communication in
10 the new-type communication process that is performed between the camera body and the photographing lens;

Figure 22 is a block diagram of fundamental elements of a communication/control system of a second embodiment of the photographing lens 200 which incorporates a lens
15 AF system;

Figure 23 is a flow chart for a main process of the second embodiment of the photographing lens;

Figure 24 is a flow chart for a 1ms-timer interrupt process of the second embodiment of the photographing
20 lens;

Figure 25 is a flow chart for the first half of an inverse-INT interrupt process of the second embodiment of the photographing lens;

Figure 26 is a flow chart for the remaining half of
25 the inverse-INT interrupt process of the second

embodiment of the photographing lens;

Figure 27 is a schematic block diagram of fundamental elements of control systems of a photographing lens, a rear converter, and a camera body 5 of an embodiment of an SLR camera system, wherein the rear converter is mounted between the camera body and the photographing lens;

Figure 28 is a timing chart for the communication identification process, from the moment the main switch 10 of the camera body is turned ON to the moment immediately after the commencement of the new-type communication process in the embodiment of the SLR camera system shown in Figure 27;

Figure 29 is a timing chart for communications in 15 the new-type communication process that is performed between the camera body and the new type of photographing lens, and between the rear converter and the camera body in the embodiment of the SLR camera system shown in Figure 27;

20 Figure 30 is a flow chart for the main process of the rear converter;

Figure 31 is a flow chart for the new-type communication setting process of the rear converter; and

25 Figure 32 is a flow chart for the inverse-INT interrupt process of the rear converter.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

Figure 1 shows fundamental elements of control systems of a camera body and an interchangeable photographing lens of an embodiment of an SLR camera system to which the present invention is applied. The camera body 100 is provided with a body CPU (body controller) 111 serving as a controller which comprehensively controls the overall operations of the SLR camera system. The camera body 100 is provided with a body mount 103 to which the photographing lens 200 is mounted. A group of communication/control contacts (body communication line) 104 are provided on the body mount 103. The group of communication/control contacts 104 consists of six contacts in this particular embodiment. One of the six contacts serves as a power contact (constant-voltage contact) for supplying a first power from the camera body 100 to low power elements (e.g., a ROM) provided in the photographing lens 200 to drive the low power elements, while another one of the six contacts serves as a control terminal via which a ROM provided in the photographing lens 200 is enabled or disabled (i.e., turned ON or OFF). A power contact 105 (VPZ) via which a second power is supplied from the camera body 100 to the photographing lens 200 is provided on the body mount

103. The power capacity of the second power that is supplied from the power contact 105 (VPZ) to the photographing lens 200 is substantially greater than that of the first power that is supplied from the 5 aforementioned constant-voltage contact of the group of communication/control contacts 104. Although the supply voltage of the second power is greater than the supply voltage of the first power, the supply voltage of the second power can be identical to the supply voltage of 10 the first power or even smaller than the supply voltage of the first power as long as the power capacity of the second power is substantially greater than the power capacity of the first power.

Although it is desirable that the group of 15 communication/control contacts 104 and the power contact 105 (VPZ) be provided on the body mount 103, the group of communication/control contacts 104 and the power contact 105 (VPZ) can be provided behind the body mount 103 in a mirror box of the camera body 100, in which a 20 quick-return mirror is positioned. Alternatively, it is possible that the group of communication/control contacts 104 be provided on the body mount 103 and the power contact 105 (VPZ) be provided behind the body mount 103 in the mirror box of the camera body 100.

25 Figure 2 shows fundamental elements of the control

system of the camera body 100. A photometering switch SWS, a release switch SWR, a main switch SWMAIN, an image-shake compensation switch SW1 and an AF switch SWAF are connected to the body CPU 111, which serves as a controller that comprehensively controls the overall operations of the SLR camera system.

The power to peripheral circuits of the camera body 100 is turned ON and OFF when the main switch SWMAIN is turned ON and OFF, respectively. The power from a battery 113 accommodated in the camera body 100 is supplied to each peripheral circuit of the camera body 100 via a regulator (DC/DC converter) 116 when the main switch SWMAIN is turned ON, and the power from the battery 113 to each peripheral circuit of the camera body 100 is cut off when the main switch SWMAIN is turned OFF. The body CPU 111 is always supplied with power from the battery 113 via the regulator 116, so that the body CPU 111 is in operation at all times.

The camera body 100 is provided with a strobe circuit 121, a mirror circuit 123, a shutter circuit 125, a film-winding circuit 127, a photometering circuit 129 and a distance measuring circuit 131, which are all connected to the body CPU 111. The photometering switch SWS is turned ON when a release button (not shown) is depressed by half a step, and the release switch

SWR is turned ON when the release button is fully depressed. Immediately after the photometering switch SWS is turned ON, the body CPU 111 actuates the photometering circuit 129 to perform a photometering operation. At the same time, the body CPU 111 calculates and sets an optimum shutter speed and an optimum aperture value (f-number), and actuates the strobe circuit 121 to perform a strobe charging process as needed. Furthermore, the body CPU 111 actuates the distance measuring circuit 131 to determine an amount of defocus, to perform an autofocus process if an autofocus mode has been set via the AF switch SWAF. Immediately after the release switch SWR is turned ON, the body CPU 111 actuates the shutter circuit 125 to drive a focal plane shutter mechanism (not shown) to expose a film frame. Upon completion of an exposure, the body CPU 111 actuates the film-winding circuit 127 to wind up film by one frame and at the same time to charge the focal plane shutter mechanism.

When the new-type photographing lens (e.g., a KAF III type photographing lens having a lens CPU, a lens ROM, and all of the communication functions which correspond with those of the camera body 100) 200 is mounted to the camera body 100, during the time the main switch SWMAIN is ON, the body CPU 111 turns ON a switch circuit 115 to supply the power from the battery 113 as the

aforementioned second power to the photographing lens 200 via the power contact 105 (VPZ) of the camera body 100 and associated power contact 205 (VPZ) of the photographing lens 200, which is in contact with the power contact 105 (VPZ). In addition, if an image-shake compensation mode has been set via the image-shake compensation switch SW1, and if the photographing lens 200 is provided with an image-shake compensation device, the body CPU 111 outputs an image-shake compensation command to the photographing lens 200 via lens communication to make the photographing lens 200 perform an image-shake compensation operation. If the photographing lens 200 mounted to the camera body 100 is further provided therein with a lens AF system, the body CPU 111 outputs defocus data (e.g., the amount of driving of an AF motor 261 (see Figure 12A) and the direction of driving of the AF motor in the photographing lens 200) to the photographing lens 200 via lens communication to make the photographing lens 200 perform a lens autofocus process.

As shown in Figure 12A, an encoder 231, the AF motor (focusing lens driving device) 261, an AF lens group (focusing lens group) Lf, and the lens CPU 211 constitute a focus adjusting system (electrical component).

The photographing lens 200 is provided on a lens

mount 203 thereof with a group of communication/control contacts (lens communication line) 204 and the power contact 205 (VPZ). The group of communication/control contacts 204 and the power contact 205 (VPZ) come into contact with the group of communication/control contacts 104 and the power contact 105 (VPZ) of the camera body 100, respectively, when the photographing lens 200 is mounted to the body mount 103 of the camera body 100 via the lens mount 203. The photographing lens 200 is provided therein with a lens CPU (LCPU/lens controller/electronic device) 211, a lens ROM (LROM/lens memory/nonvolatile lens memory) 221, the encoder 231 and a peripheral circuit 241. Various modes and parameters are stored in the lens ROM 221. A current focal length (zoom code) and a photographic distance are detected via the encoder 231. The peripheral circuit 241 includes, for example, as shown in Figure 12A, image-shake compensation motors (X-motor 254 and Y-motor 257), the AF motor 261, and a power zoom motor (power zoom driving device) 264, which are all provided in the photographing lens 200. Note that the power zoom motor 264 is connected to a lens group Lz, wherein the lens group Lf and the lens group Lz constitute at least part of a zoom lens system of the photographing lens 200.

The group of communication/control contacts 104 of

the camera body 100 consists of six contacts: a first contact 104a (Fmin1/ Inverse-SCKL), a second contact 104b (Fmin2/ DATA1), a third contact 104c (Fmin3/ RESL), a fourth contact 104d (CONT1), a fifth contact 104e (Fmax1/ 5 Inverse-FBL) and a sixth contact 104f (Fmax2/ Inverse-FLB). Likewise, the group of communication/control contacts 204 of the photographing lens 200 consists of six contacts: a first contact 204a (Fmin1/ Inverse-SCKL), a second contact 204b (Fmin2/ 10 DATA1), a third contact 204c (Fmin3/ RESL), a fourth contact 204d (CONT1), a fifth contact 204e (Fmax1/ Inverse-FBL) and a sixth contact 204f (Fmax2/ Inverse-FLB) which come into contact with the first through sixth contacts 104a through 104f respectively, 15 when the photographing lens 200 is mounted to the camera body 100.

The power line from port P13 of the body CPU 111 to the fourth contact 104d constitutes a first body power line for supplying the first power to the photographing lens 200. The power line from the battery 113 to the power contact 105 (via the switch circuit 115) constitutes a 20 second body power line for supplying the second power to the photographing lens 200. The power line from the fourth contact 204d to a port CONT of the lens ROM 221 constitutes a first lens power line for supplying power 25

from the camera body 100 to the photographing lens 200. The power line from the power contact 205 to a regulator 243 and to a switching circuit 242 constitutes a second lens power line for supplying power from the camera body 5 100 to the lens CPU 211. As shown in Figure 1, the second power that is output from the camera body 100 to be input to the photographing lens 200 via the power contacts 105 and 205 (VPZ) is supplied to the lens CPU 211 via the regulator 243 of the photographing lens 200 and also to 10 the peripheral circuit 241 via a switching circuit 242 of the photographing lens 200. The lens ROM 221 of the photographing lens 200 operates with constant voltage power (the first power) supplied from the fourth contact 204d (CONTL), whereas the lens CPU 211 operates with the 15 second power having a large power capacity supplied from the power contact (VPZ) 205. The processing speed and the throughput of a CPU is generally proportional to the power consumption of the CPU. Accordingly, in the present embodiment of the SLR camera system to which the 20 present invention is applied, providing the second power having a large power capacity to the photographing lens 200 makes it possible for the photographing lens 200 be provided therein with not only a CPU which achieves a high throughput, but also high power components (i.e., 25 components which require a large current) such as a lens

motor and an image-shake compensation device.

Figure 3 is a block diagram of fundamental elements of a communication/control system of the photographing lens 200. The first contact 204a (Fmin1/ Inverse-SCKL),
5 the second contact 204b (Fmin2/ DATA), the third contact 204c (Fmin3/ RESL), and the fourth contact 204d (CONT) of the group of communication/control contacts 204, of
the photographing lens 200, are connected to four ports
RES, SIO, Inverse-SCK, and CONT of the lens ROM 221,
10 respectively.

The port RES of the lens ROM 221 serves as an input port via which the lens ROM 221 inputs a reset signal that changes the state of the lens ROM 221 from a disabled state to an enabled state. The port SIO of the lens ROM 221
15 serves as an I/O port for serial communication. The port Inverse-SCK of the lens ROM 221 serves as an input port via which the lens ROM 221 inputs a clock signal for communication from the camera body 100. The port CONT of the lens ROM 221 serves as an input port via which the
20 lens ROM 221 inputs a constant voltage power (the first power) from the camera body 100.

The lens ROM 221 operates in accordance with the first power (constant voltage power), which is supplied from the camera body 100 to be applied to the port CONT
25 of the lens ROM 221. The lens ROM 221 is set to change

the state of the lens ROM 221 from a disabled state to an enabled state by a reset signal, which is input via the port RES of the lens ROM 221 to enter the enabled state. Lens data written in the lens ROM 221 is read out therefrom
5 to be output to the camera body 100 via the port SIO of the lens ROM 221, in synchronization with the clock signal input, via the port Inverse-SCK. The port RES of the lens ROM 221 and the third contact 204c (Fmin3/ RESL), which is connected to the port RES, also serve as a control line
10 for changing the state of the lens ROM 221 between an enabled state and a disabled state. Namely, the lens ROM 221 operates while the first power is being supplied to the fourth contact 204d (CONTL), and the lens ROM 221 is set to change the state thereof from a disabled state to
15 an enabled state if the level of the third contact 204c (Fmin3/ RESL) falls to a low level, and the lens ROM 221 is set to change the state of the lens ROM 221 from an enabled state to a disabled state if the level of the third contact 204c (Fmin3/ RESL) rises to a high level. The
20 timing chart thereof is shown in Figure 20.

As shown in Figure 3, the lens CPU 211 is provided with eight ports RXD, TXD, TXDEN, Inverse-SCK, P00, P01, INT and VCC, and the photographing lens 200 is provided with first through fourth high input voltage proof Schmitt
25 inverters VCC1, VCC2, VCC3 and VCC4. The first contact

204a (Fmin1/ Inverse-SCKL) is connected to the port
Inverse-SCK of the lens CPU 211 via the second and third
Schmitt inverters VCC2 and VCC3, and the second contact
204b (Fmin2/ DATA1) is connected to the port RXD of the
5 lens CPU 211 and also to each of the two ports TXD and
TXDEN of the lens CPU 211 via a first I/O protection
circuit 212.

The port RXD of the lens CPU 211 serves as a data
input port. The port TXD of the lens CPU 211 serves as
10 a data output port. The port TXDEN of the lens CPU 211
serves as a control port via which the lens CPU 211
determines whether data can be output from the port TXD
of the lens CPU 211. The port Inverse-SCK of the lens
CPU 211 serves as an input port via which the lens CPU
15 211 inputs a clock signal for communication from the
camera body 100.

When the control port TXDEN of the lens CPU 211 is
at a high level, if the level of a data output port TXD
of the lens CPU 211 rises to a high level, a field effect
20 transistor (FET) of the first I/O protection circuit 212
is turned OFF while a transistor of the first I/O
protection circuit 212 is turned ON to thereby cause the
level of port 212a to rise to a high level. On the other
hand, if the level of the data output port TXD of the lens
25 CPU 211 falls to a low level when the control port TXDEN

of the lens CPU 211 is at a high level, the field effect transistor (FET) of the first I/O protection circuit 212 is turned ON while the transistor of the first I/O protection circuit 212 is turned OFF to thereby cause the
5 level of port 212a to fall to a low level. Therefore, when the control port TXDEN of the lens CPU 211 is at a high level, the level of the data output port TXD of the lens CPU 211 is output from the first I/O protection circuit 212 via port 212a to be input to the second contact
10 204b (Fmin2/ DATA).

Because each of the field effect transistor (FET) and the transistor of the first I/O protection circuit 212 is OFF when the control port TXDEN is at a low level, port 212a is in a high impedance state regardless of the
15 level of the data output port TXD of the lens CPU 211.

The sixth contact 204f (Fmax2/ Inverse-FLB) is connected to each of the two ports P00 and P01 of the lens CPU 211 via a second I/O protection circuit 213, while the fifth contact 204e (Fmax1/ Inverse-FBL) is connected
20 to the port INT of the lens CPU 211 via the fourth high input voltage proof Schmitt inverter VCC4. The port P00 of the lens CPU 211 serves as an output port while the port P01 of the lens CPU 211 serves as a control port via which the lens CPU 211 determines whether data can be
25 output from the port P00. The port INT of the lens CPU

211 serves as an input port via which the lens CPU 211 inputs an interrupt signal.

When the control port P01 of the lens CPU 211 is at a high level, if the level of the output port P00 of the lens CPU 211 rises to a high level, a field effect transistor (FET) of the second I/O protection circuit 213 is turned OFF while a transistor of the second I/O protection circuit 213 is turned ON to thereby cause the level of port 213a to rise to a high level. On the other hand, if the level of the output port P00 of the lens CPU 211 falls to a low level when the control port P01 of the lens CPU 211 is at a high level, the field effect transistor (FET) of the second I/O protection circuit 213 is turned ON while the transistor of the second I/O protection circuit 213 is turned OFF to thereby cause the level of port 213a to fall to a low level. Therefore, when the output port P00 of the lens CPU 211 is at a high level, the level of the output port P00 of the lens CPU 211 is output from the second I/O protection circuit 213 via port 213a to be input to the sixth contact 204f (Fmax2/Inverse-FLB).

Because each of the field effect transistor (FET) and the transistor of the second I/O protection circuit 213 is OFF when the control port P01 is at a low level, port 213a is in a high impedance state regardless of the

level of the output port P00 of the lens CPU 211.

The power contact 205 (VPZ) is connected to the power port VCC of the lens CPU 211 via a regulator 243. The lens CPU 211 operates with constant voltage supplied from 5 the regulator 243 to the power port VCC.

Selection between the communication channel for communication of the body CPU 111 with the lens ROM 221 (i.e., lens ROM communication/old-type communication) and the communication channel for communication of the 10 body CPU 111 with the lens CPU 211 (i.e., new-type communication) depends on a reset signal input to the third contact 204c (Fmin3/ RESL). If the level of the input port RES of the lens ROM 221 rises to a high level, the lens ROM 221 enters a disabled state and the SIO port 15 of the lens ROM 221 enters a high impedance state. This makes the aforementioned new-type lens communication between the body CPU 111 and the lens CPU 211 possible.

The first contact 204a (Fmin1/ Inverse-SCKL), the second contact 204b (Fmin2/ DATAL), the third contact 204c 20 (Fmin3/ RESL), the fifth contact 204e (Fmax1/ Inverse-FBL) and the sixth contact 204f (Fmax2/ Inverse-FLB) maintain compatibility with conventional camera systems using interchangeable lenses in which serial communication between camera body and 25 interchangeable lens is performed without using a ROM

(lens ROM) provided in an interchangeable lens. For instance, in order to maintain compatibility with a camera body which can obtain the minimum f-number and the maximum f-number from the photographing lens mounted to the camera body, diodes for making the first, second, third, fifth and sixth contacts 204a, 204b, 204c, 204e and 204f serve as aperture information contacts so that the camera body can input data on the minimum f-number (the f-number at maximum aperture) via the first, second and third contacts 204a, 204b and 204c and so that the camera body can input data on the maximum f-number (the f-number at minimum aperture) via the fifth and sixth contacts 204e and 204f. 10 are selectively provided in a manner such that the camera body can distinguish between the maximum f-number and the minimum f-number by checking continuity of each contact 15 via the diodes.

Figure 4 is a block diagram of fundamental components of a photographing lens 200a which is provided with a lens CPU 211a and a peripheral circuit 241a. The lens CPU 211a 20 operates with the first power supplied from the fourth contact 204d (CONTL), while the peripheral circuit 241a operates with power supplied from the power contacts 105 and 205 (VPZ). In the photographing lens 200a shown in Figure 4, the power supplied from the power contacts 105 and 205 (VPZ) is supplied to the peripheral circuit 241a

via the switching circuit 242.

Figure 5 is a block diagram of fundamental components of a photographing lens 200b which is provided with a lens CPU 211b and a peripheral circuit 241b. The lens CPU 211b operates with the first power supplied from the fourth contact 204d (CONTL). The photographing lens 200b shown in Figure 5 is not provided with either a power contact or a regulator corresponding to the power contact 205 (VPZ) or the regulator 243, respectively. Each of the lens CPU 211b and the peripheral circuit 241b operates with the first power supplied from the fourth contact 204d (CONTL).

In the photographing lens 200a shown in Figure 4, the camera body 100 supplies the first power and the second power to the fourth contacts 104d and 204d (CONTL) and the power contacts 105 and 205 (VPZ), respectively. On the other hand, in the photographing lens 200b shown in Figure 5, the camera body 100 supplies only the first power to the fourth contacts 104d and 204d (CONTL).

Fundamental operations of the camera body 100 and the photographing lens 200 will be hereinafter discussed in detail with reference to the flow charts shown in Figures 6 through 11 and the timing charts shown in Figures 18 through 21B. Figure 6 shows a flow chart for the main process of the camera body 100 which is performed by the

body CPU 111. Control enters the main process immediately after the battery 113 is loaded into the camera body 100. The camera body 100 performs both old-type communication (lens ROM communication) and new-type communication between the camera body 100 and the photographing lens 200, whereas the camera body 100 performs only the old-type communication (lens ROM communication) between the camera body 100 and the photographing lens 200 if the photographing lens 200 is of any other type which is not provided with any CPU corresponding to the lens CPU 211 of the photographing lens 200 but provided with only a lens ROM, and which accordingly does not have any communication capability of the photographing lens 200. It should be noted that operations or processes having step numbers bearing a prefix "CS" are related to control/operation of the camera body 100 and that operations or processes having step numbers bearing a prefix "LS" are related to control/operation of the photographing lens 200.

Fundamental commands for discussion of the present embodiment of the SLR camera system are listed below. All the commands listed below are those which are transmitted from the camera body 100 to the photographing lens 200.

[COMMANDS TRANSMITTED FROM CAMERA BODY TO LENS IN ORDER

[TO COMMAND LENS TO TRANSMIT DATA TO CAMERA BODY]

70: Command for making the photographing lens send a lens status thereof to the camera body.

71: Command for making the photographing lens send
5 a lens status thereof to the camera body and for making
the lens CPU enter a sleep mode together with the body
CPU.

72: Command for making the photographing lens send
information on functions that the photographing lens
10 possesses, such as an image-shake compensation function
and a lens autofocus function, to the camera body.

7F: Command for a rear converter.

[COMMANDS FOR DATA TRANSMISSION FROM CAMERA BODY TO LENS]

B0: Command for sending data to the photographing
15 lens.

B1: Command for sending data to the photographing
lens and for making the lens CPU enter a sleep mode.

B2: Command for sending data on a driving-amount for
the AF motor provided in the photographing lens to the
20 photographing lens.

[INSTRUCTION COMMANDS TRANSMITTED FROM CAMERA BODY TO
LENS]

D0: Command for making the lens CPU enter the sleep
mode.

25 D1: Command for turning OFF an image-shake

compensation function.

D2: Command for turning ON the image-shake compensation function.

D3: Command for stopping the driving of the AF motor
5 provided in the photographing lens.

D4: Command for resuming the driving of the AF motor provided in the photographing lens.

In the main process shown in Figure 6, firstly it is determined whether the main switch SWMAIN is ON (step 10 CS101). The operation at step CS101 is repeated until the main switch SWMAIN is turned ON. If the main switch SWMAIN is turned ON (if YES at step CS101), a communication identification process (i.e., an old-type communication process at step CS103 and a new-type 15 communication setting request process at step CS105) is performed. Command 72 is transmitted to the photographing lens to receive data therefrom (step CS107). Command 72 commands the lens CPU 211 to output information on the functions that the photographing lens possesses 20 to the body CPU 111. The functions of the photographing lens can include, e.g., an image-shake compensation function, a lens autofocus function and other functions which operate with power (the second power) supplied from the power contacts VPZ. In the present embodiment of the 25 SLR camera system, such information on functions that the

photographing lens has is represented by 1-byte data (8-bit data), wherein the sixth bit represents the presence or absence of the autofocus function while the fourth bit represents the present or absence of the 5 image-shake compensation function. Upon receiving command 72, the lens CPU 211 of the (new-type)photographing lens 200 outputs information on functions that the photographing lens 200 possesses to the body CPU 111. Figure 18 shows a timing chart for the 10 aforementioned communication identification process from the moment the main switch SWMAIN is turned ON to the moment immediately after the commencement of the new-type communication process. Figure 19A and 19B each show a timing chart for a handshake operation performed 15 between the camera body 100 and the photographing lens 200 at the commencement of the communication process. Figure 20 shows a timing chart for the old-type communication process. Figures 21A and 21B show timing charts for the new-type communication process.

20 After the operation at step CS107, the body CPU 111 sets a lens sleep flag SLP to "0" (step CS109). The lens sleep flag SLP "1" or "0" indicates that the lens CPU 211 is in the sleep mode (low power operation mode) or not in the sleep mode, respectively. The operations or 25 processes at steps CS103 through CS109 are performed when

the main switch SWMAIN is turned from OFF to ON. Thereafter, the operations at and after step CS111 are repeated.

At step CS111, ON/OFF states of all the switch ports
5 are input. Subsequently, a camera state information setting process is performed (step CS113). In the camera state information setting process, the information on the current states of some specific switches and a flash charging system which is to be transmitted to the lens
10 CPU 211 via the new-type communication is prepared.

Subsequently, the old-type communication process is performed (step CS115), and it is determined whether a photographing lens is mounted to the camera body 100 (step CS117). If no photographing lens is mounted (if YES at
15 step CS117), each of the fourth contact 104d (CONTL) and the power contact 105 (VPZ) is set to a low level (step CS119), and control returns to step CS101. If it is determined at step CS117 that a photographing lens is mounted (if NO at step CS117), it is determined whether
20 a new-type flag is "1", i.e., whether the photographing lens currently mounted to the camera body 100 is the (new-type) photographing lens 200 (step CS121). The new-type flag "1" indicates that the photographing lens currently mounted to the camera body 100 is the (new-type)
25 photographing lens 200. If the new-type flag is

"1" (if YES at step CS121), it is determined whether a power hold flag PH is "0", i.e., whether the camera body 100 is not in a power hold state (step CS123). If the power hold flag PH is "0" (if YES at step CS123), it is
5 determined whether the lens sleep flag SLP is "1" (step CS125). If the lens sleep flag SLP is "1" (if YES at step CS125), control returns to step CS111 since the photographing lens 200 is already in a sleep mode. If it is determined at step CS125 the lens sleep flag SLP
10 is not "1", command B1 is transmitted to the lens CPU 211 to make the photographing lens 200 enter the sleep mode (step CS127). Subsequently, the lens sleep flag SLP is set to "1" (step CS129), and control returns to step CS111.

If it is determined at step CS123 that the power hold flag PH is not "0" (if NO at step CS123), command B0 is transmitted to the lens CPU 211 to start the lens CPU 211 (step CS131), and the lens sleep flag SLP is set to "0" (step CS133).

Subsequently, it is determined whether a rear converter 300 (see Figure 27) is mounted between the camera body and the photographing lens via the result of the old-type communication process at step CS115 (step CS134a). If the rear converter 300 is mounted between the camera body and the photographing lens 200 (if YES
25 at step CS134a), command 7F, which commands the rear

converter 300 to send information (data) thereon to the camera body, is transmitted to the camera body 100 (step CS134b), and control proceeds to step CS135. If no rear converter is mounted between the camera body and the 5 photographing lens (if NO at step CS134a), control proceeds straight from step CS134a to step CS135. At step CS135 it is determined whether an image-shake compensating type lens flag is "1", i.e., whether the photographing lens 200 mounted to the camera body 100 is 10 provided with an image-shake compensation device. If the image-shake compensating type lens flag is "1" (if YES at step CS135), an image-shake compensation data setting process, in which predetermined flags and data on image-shake compensation are set, is performed (step 15 CS137), and control proceeds to step CS139. If the image-shake compensating type lens flag is not "1" (if NO at step CS135), control skips step CS137 to proceed straight from step CS135 to step CS139. If it is determined at step CS121 that the new-type flag is not 20 "1", control proceeds straight from step CS121 to step CS139.

At step CS139 it is determined whether a SWMAIN flag is "0", i.e., whether the main switch SWMAIN has been turned from ON to OFF. If it is determined at step CS139 25 that the SWMAIN flag is not "1" (if NO at step CS139),

it is determined whether the photometering switch SWS is ON (step CS141). If the photometering switch SWS is not ON (if NO at step CS141), control returns to step CS111.

If the photometering switch SWS is ON (if YES at step 5 CS141), control proceeds to step CS151. If it is determined at step CS139 that the SWMAIN flag is "1", it is determined whether the new-type flag is "1" (step CS143). If it is determined at step CS143 that the new-type flag is not "1", control returns to step CS101.

10 If it is determined at step CS143 that the new-type flag is "1" (i.e., that the (new-type) photographing lens 200 is currently mounted to the camera body 100), it is determined at step CS145 whether a second power flag VpzONCPU is "1", i.e., whether the photographing lens 200

15 mounted to the camera body 100 is of a type which operates with power supplied from the power contact 105 (VPZ). If it is determined at step CS145 that the second power flag VpzONCPU is "1" (if YES at step CS145), the port VPZ is turned OFF, i.e., power supplied to the power contact 105 (VPZ) is cut off (step CS147), and control returns to step

20 CS101. If it is determined at step CS145 that the second power flag VpzONCPU is not "1" (if NO at step CS145), control returns to step CS101 since the photographing lens mounted to the camera body 100 does not operate with power

25 supplied from the power contact 105 (VPZ).

Operations which are performed after it is determined at step CS141 that the photometering switch SWS is ON will be hereinafter discussed with reference to the flow chart shown in Figure 7.

5 If it is determined at step CS141 that the photometering switch SWS is ON (if YES at step CS141), a photometering operation in which photometric data is input from a photometering sensor and an exposure arithmetic operation are performed in accordance with a
10 currently-selected photometering mode and a currently-selected exposure mode, respectively (step CS151). Subsequently, AF sensor data is input from an AF sensor in accordance with a currently-selected AF mode, while a predetermined AF arithmetic operation necessary
15 for attaining an in-focus state is performed in accordance with the input AF sensor data (step CS153).

Subsequently, it is determined whether the new-type flag is "1" (step CS155). If the new-type flag is "1" (if YES at step CS155), it is determined whether a lens AF flag is "1", i.e., whether the photographing lens 200 mounted to the camera body 100 has a lens autofocus function (step CS157). If the lens AF flag is "1" (if YES at step CS157), it is determined whether an AFON flag is "1" (step CS159). The AFON flag "1" indicates that
25 the AF function is ON, i.e., the AF function is in

operation. If the AFON flag is "1" (if YES at step CS159), data on a driving-amount of an AF lens (focusing lens group) of the photographing lens is transmitted to the lens CPU 211 (step CS161), and subsequently control 5 proceeds to step CS163. If at least one of the new-type flag, the lens AF flag and the AFON flag is not "1", control skips the operation at step CS161 and proceeds to step CS163.

At step CS163 it is determined whether an in-focus state has been obtained. If an in-focus state has not 10 been obtained (if NO at step CS163), control returns to step CS111 shown in Figure 6. Accordingly, in the present embodiment of the SLR camera system, an in-focus priority control in which the shutter cannot be released unless 15 an in-focus state is obtained is adopted. A release priority control in which the shutter can be released even in an out-of-focus state can be adopted. In this case, the operation at step CS163 is omitted.

If it is determined at step CS163 that an in-focus state has been obtained (if YES at step CS165), it is 20 determined whether the release switch SWR is ON (step CS165). If the release switch SWR is OFF (if NO at step CS165), control returns to step CS111.

If the release switch SWR is ON (if YES at step CS165), 25 it is determined whether the new-type flag is 1 (step

CS167). If the new-type flag is 1 (if YES at step CS167), a release stage indicator RLS is set to "1", and information on the indicator RLS "1" is transmitted to the lens CPU 211 (step CS169). Subsequently control 5 proceeds to step CS171. If the new-type flag is not 1 (if NO at step CS167), control skips step CS169 to proceed straight from step CS167 to step CS171, so that the information on the indicator RLS "1" is not transmitted to the lens CPU 211. The release stage indicator RLS "1" 10 informs the photographing lens 200 of a stage at which the quick return mirror is moving toward the retracted position thereof after the release switch SWR has been turned ON.

At step CS171 the mirror circuit 123 is actuated to 15 drive a mirror drive motor so that the quick return mirror of the camera body 100 moves up to a retracted position. Subsequently, it is determined whether the new-type flag is 1 (step CS173). If the new-type flag is 1 (if YES at step CS173), the release stage indicator RLS is set to 20 "2", and information on the stage indicator RLS "2" is transmitted to the lens CPU 211 (step CS175). Subsequently, control proceeds to step CS177. If the new-type flag is not 1 (if NO at step CS173), control skips step CS175 to proceed straight from step CS173 to step 25 CS177, so that the information on the indicator RLS "2"

is not transmitted to the lens CPU 211. The release stage indicator RLS "2" informs the photographing lens 200 of a stage at which a film frame is under exposure after the quick return mirror has moved up to the retracted position

5 thereof.

At step CS177 the shutter circuit 125 is actuated to drive the focal plane shutter mechanism to perform an exposure operation. Upon completion of the exposure operation, it is determined whether the new-type flag is

10 1 (step CS179). If the new-type flag is 1 (if YES at step CS179), the release stage indicator RLS is set to "3", and information on this indicator RLS "3" is transmitted to the lens CPU 211 (step CS181). Subsequently control proceeds to step CS183. If the new-type flag is not 1

15 (if NO at step CS179), control skips step CS181 to proceed straight from step CS179 to step CS183, so that the information on the indicator RLS "3" is not transmitted to the lens CPU 211. The release stage indicator RLS "3" informs the photographing lens 200 of a stage at which

20 film is wound after the exposure operation has been completed.

At step CS183 a film-winding operation in which the film-winding circuit 127 is actuated to drive a film motor (shutter charge motor) to wind film by one frame is

25 performed while a shutter charge operation is performed

(step CS183). Subsequently, it is determined whether the new-type flag is "1" (step CS185). If the new-type flag is 1 (if YES at step CS185), the release stage indicator RLS is set to "0", and information on this indicator RLS 5 "0" is transmitted to the lens CPU 211 (step CS187). Subsequently control returns to step CS111. If the new-type flag is not 1 (if NO at step CS185), control skips step CS187 to return straight from step CS185 to step CS111, so that the information on the indicator RLS "0" is not 10 transmitted to the lens CPU 211. The release stage indicator RLS "0" informs the photographing lens of a stage at which the aforementioned film-winding operation has been completed, i.e., a state at which the shutter can be released.

15 In the above described release process at and after step CS151, the release stage indicator RLS that indicates a stage in the release process is transmitted to the lens CPU 211 each time each stage in the release process is completed, if the (new-type) photographing 20 lens 200 is mounted to the camera body 100. This makes it possible for the photographing lens 200 to perform operations which correspond to operational state and stage of the camera body 100.

The communication identification process, which is 25 composed of the old-type communication process at step

CS103 and the new-type communication setting request process at step CS105, will be hereinafter discussed in detail with reference to the flow charts shown in Figures 8A and 8B. Figure 8A shows operations of the 5 communication identification process, while Figure 8B shows operations of the photographing lens 200 which are performed by the lens CPU 211 in accordance with operations of the new-type communication setting request process at step CS105. The new-type communication 10 setting request process at step CS105 includes operations at steps CS203 through CS215 shown in Figure 8A.

Control enters the communication identification process immediately after the power of the camera body 100 is turned ON. The communication identification 15 process is performed to identify the type of photographing lens 200 and communication protocols used therefor. Immediately after the power of the camera body 100 is turned ON (i.e., immediately after it is determined that the main switch SWMAIN is ON at step CS101), control enters 20 the old-type communication process at step CS103. In the old-type communication process at step CS103, it is determined whether the photographing lens currently mounted to the camera body 100 is provided with a lens ROM from which the body CPU 111 can read out any 25 predetermined lens data, and subsequently the old-type

communication (lens ROM communication) is performed in accordance with communication protocols used for the photographing lens having such a lens ROM, if it is determined that the photographing lens currently mounted to the camera body 100 is provided with such a lens ROM.

In the lens ROM communication, predetermined lens data written in the lens ROM 221 are read therefrom. This lens data includes data on the lens type of the currently mounted photographing lens.

Upon completion of the lens ROM communication, it is determined, from the result of the lens ROM communication, whether the photographing lens 200 currently mounted to the camera body 100 is a new type of photographing lens (step CS203). If it is determined that a new type of photographing lens is not mounted to the camera body 100 (if NO at step CS203), control exits the communication identification process, and from this time on only the old-type communication (lens ROM communication) is performed between the photographing lens 200 and the camera body 100.

If it is determined at step CS203 that the photographing lens 200 mounted to the camera body 100 is a new type of photographing lens (if YES at step CS203), it is determined whether the currently-mounted (new-type) photographing lens 200 is of a type (VpzON type)

which operates with power supplied from the power contact 105 (VPZ) (step CS205). If the currently-mounted photographing lens 200 is a VpzON type lens (if YES at step CS205), the power contact 105 (VPZ) is turned ON; 5 namely, power is supplied to the power contact 105 (VPZ) (step CS207). Subsequently, control proceeds to step CS209. On the other hand, if the currently-mounted photographing lens 200 is not a VpzON type lens (if NO at step CS205), control skips step CS207 to proceed 10 straight from step CS205 to step CS209, so that no power is supplied to the power contact 105 (VPZ).

At step CS209 the level of the fifth contact 104e (Fmax1/ Inverse-FBL) is made to fall to a low level ("Lo" or "L" level), and subsequently it is determined whether 15 the level of the sixth contact 104f (Fmax2/ Inverse-FLB) is a low level (step CS211). The operation at step CS211 is repeated as long as the level of the sixth contact 104f (Fmax2/ Inverse-FLB) is a high level. If it is determined at step CS211 that the level of the sixth contact 104f 20 (Fmax2/ Inverse-FLB) is a low level (if YES at step CS211), the level of the fifth contact 104e (Fmax1/ Inverse-FBL) is raised to a high level ("Hi" or "H" level) (step CS213), and subsequently it is determined whether the level of the sixth contact 104f (Fmax2/ Inverse-FLB) is a high 25 level (step CS215). The operation at step CS215 is

repeated as long as the level of the sixth contact 104f (Fmax2/ Inverse-FLB) is a low level. If it is determined at step CS215 that the level of the sixth contact 104f (Fmax2/ Inverse-FLB) is a high level (if YES at step CS215),
5 this means that the (new type of) photographing lens 200 mounted to the camera body 100 operates normally, so that control comes out of the communication identification process, and from this time on new-type communication is performed between the photographing lens 200 (the lens
10 CPU 211) and the camera body 100 (the body CPU 111).

On the other hand, while the camera body 100 performs the operations at steps CS207 through CS215, the (new-type) photographing lens 200 performs the operations represented by the flow chart shown in Figure
15 8B. If the power contact 105 (VPZ) is supplied with power at step CS207, this power is supplied to the photographing lens 200 via the power contact 205 (VPZ). This causes the regulator 243 to supply a constant voltage to the lens CPU 211, which in turn causes the lens CPU 211 to
20 initialize internal RAM thereof (step LS201). Subsequently, it is determined whether the level of the fifth contact 204e (Fmax1/ Inverse-FBL) is a low level (step LS203). The operation at step LS203 is repeated as long as the level of the fifth contact 204e (Fmax1/
25 Inverse-FBL) is a high level. If it is determined, via

the fifth contact 204e (Fmax1/ Inverse-FBL) and the port INT of the lens CPU 211, that the level of the fifth contact 104e (Fmax1/ Inverse-FBL) falls to a low level due to the operation at step CS209 (if YES at step LS203), the level 5 of the sixth contact 204f (Fmax2/ Inverse-FLB) is made to fall to a low level via the port P00 of the lens CPU 211 (step LS205). Thereafter, it is determined whether the level of the fifth contact 204e (Fmax1/ Inverse-FBL) is a high level (step LS207). The operation at step LS207 10 is repeated as long as the level of the fifth contact 204e (Fmax1/ Inverse-FBL) is a low level. If it is determined that the level of the fifth contact 104e (Fmax1/ Inverse-FBL) rises to a high level due to the operation at step CS213 (if YES at step LS207), the level of the 15 sixth contact 204f (Fmax2/ Inverse-FLB) is raised to a high level (step LS209). Subsequently, control comes out of the communication identification process, and from this time on the new-type communication is performed between the (new-type) photographing lens 200 (the lens 20 CPU 211) and the camera body 100 (the body CPU 111). 20

Figure 18 shows a timing chart for the communication identification process that is performed between the body CPU 111 of the camera body 100 and the lens CPU 211 of the photographing lens 200. In the communication 25 identification process, the fifth contacts 104e and 204e

(Fmax1/ Inverse-FBL) and the sixth contacts 104f and 204f (Fmax2/ Inverse-FLB) are used to serve as handshake connectors/lines (see Figures 19A and 19B). Immediately after the power of the camera body 100 is turned ON, the 5 body CPU 111 makes the level of the fourth contact 204d (CONTL) rise to a high level to perform the old-type communication (lens ROM communication).

[OLD-TYPE COMMUNICATION (LENS ROM COMMUNICATION)]

Figure 20 shows a timing chart for the old-type 10 communication process between the camera body 100 and the photographing lens 200, i.e., between the body CPU 111 and the lens ROM 221. In the lens ROM communication, predetermined lens data written in the lens ROM 221 are read therefrom. The levels of the first contact 104a 15 (Fmin1/ Inverse-SCKL), the third contact 104c (Fmin3/ RESL) and the fourth contact 104d (CONTL) of the group of communication/control contacts 104 of the camera body 100 before the commencement of the lens ROM communication are a high level, a high level and a low level, 20 respectively. The second contact 104b (Fmin2/ DATAL) before the commencement of the lens ROM communication is in a high impedance (floating) state.

The body CPU 111 makes the level of the fourth contact 104d (CONTL) rise to a high level to actuate the lens ROM 25 221 when starting the lens ROM communication.

Subsequently, after waiting a predetermined period of time necessary for the lens ROM to operate with stability, the body CPU 111 makes the level of the third contact 104c (Fmin3/ RESL) fall to a low level to change a state of 5 the lens ROM 221 from a disabled state to an enabled state. Thereafter, if the body CPU 111 outputs a clock signal from the first contact 104a (Fmin1/ Inverse-SCKL), the lens ROM 221 reads out predetermined data from an internal ROM thereof to output the predetermined data to the second 10 contact 204b (Fmin2/ DATA1), so that the body CPU 111 inputs the predetermined data via the second contact 104b (Fmin2/ DATA1). The body CPU 111 makes the level of the third contact 104c (Fmin3/ RESL) rise to a high level upon having input a predetermined number of bytes of lens 15 data. In the present embodiment of the SLR camera system, the last one or few bytes of the lens data are spare bytes. These spare bytes can receive data (rear converter data) output from a ROM (memory/rear converter memory) 321 provided in the rear converter 300 when the rear converter 20 300 is mounted between the camera body and the photographing lens. More specifically, when the lens ROM communication (the old-type communication) is performed with the rear converter 300 being mounted between the camera body 100 and the photographing lens 25 200, as shown in Figure 27, a predetermined number of bytes

serving as the aforementioned spare bytes of the lens data stored in the lens ROM 221 are in a floating state with dummy data so that the spare bytes are transmitted to the camera body 100 with the data output from the ROM 321 of the rear converter 300 being added to the spare bytes.

5 The body CPU 111 can determine whether the rear converter 300 is connected between the camera body 100 and the photographing lens 200 in accordance with the input data of the spare bytes.

10 Information on the lens type is included in data obtained via the above described lens ROM communication, and includes data (new-type lens bit = "1") for identification of the new-type photographing lens (i.e., a lens which can perform the new-type communication) and 15 data (VpzONCPU bit = "1") for identification of the necessity for power supply. The body CPU 111 of the camera body 100 identifies whether or not the photographing lens 200 mounted to the camera body 100 is a new type of photographing lens from such data.

20 [NEW-TYPE COMMUNICATION]

Upon completion of the old-type communication, the body CPU 111 starts supplying power to the power contacts 105 and 205 (VPZ). Subsequently, the body CPU 111 makes the level of the fifth contact 104e (204e) (Fmax1/ 25 Inverse-FBL) fall to a low level to interrupt the lens

CPU 211, and waits for the level of the sixth contact 104f (204f) (Fmax2/ Inverse-FLB) to fall to a low level, i.e., waits for the lens CPU 211 to make the level of the sixth contact 204f (Fmax2/ Inverse-FLB) fall to a low level.

- 5 The fifth contact 104e (204e) (Fmax1/ Inverse-FBL), the sixth contact 104f (204f) (Fmax2/ Inverse-FLB), and the second contact 104b (204b) (Fmin2/ DATA1) correspond to a first communication/control contact, a second communication/control contact and an data I/O contact, 10 respectively.

Upon the interrupt by the body CPU 111, the lens CPU 211 "wakes up" and operates normally if in the sleep mode, and initializes the internal RAM thereof. Subsequently, upon completion of the initializing operation, the lens CPU 211 makes the level of the sixth contact 204f (Fmax2/ Inverse-FLB) fall to a low level, and waits for the level of the fifth contact 204e (104e) (Fmax1/ Inverse-FBL) to rise to a high level.

Immediately after the level of the sixth contact 104f (204f) (Fmax2/ Inverse-FLB) falls to a low level, the body CPU 111 makes the level of the fifth contact 104e (204e) (Fmax1/ Inverse-FBL) rise to a high level, and waits for the level of the sixth contact 104f (204f) (Fmax2/ Inverse-FLB) to rise to a high level.

25 Immediately after the level of the fifth contact 204e

(104e) (Fmax1/ Inverse-FBL) rises to a high level, the lens CPU 211 makes the sixth contact 204f (104f) (Fmax2/ Inverse-FLB) rise to a high level to complete the communication identification process.

5 Upon identifying that the level of the sixth contact 104f (204f) (Fmax2/ Inverse-FLB) has risen to a high level, the body CPU 111 completes the communication identification process.

From this time on, data and commands are transmitted
10 between the camera body 100 and the photographing lens 200 via the new-type communication.

The camera state information setting process performed at step CS113 will be hereinafter discussed in detail with reference to the flow chart shown in Figure 15 10. In the camera state information setting process, the information on the current states of specific switches and a flash charging system which is to be transmitted to the lens CPU 211 via the new-type communication is prepared. Specifically, in the present embodiment of the 20 SLR camera system, it is determined whether the autofocus system of the camera body 100 is in operation, whether an electronic flash (strobe) is in the middle of charging, whether a power hold timer has expired since the photometering switch SWS is turned OFF, and whether the 25 main switch SWMAIN is ON, wherein flags which indicate

these states are set as state information.

In the camera state information setting process, it is determined whether the photometering switch SWS is ON (step CS301). If the photometering switch SWS is ON (if YES at CS301), it is determined whether the AF switch SWAF is ON, i.e., whether the autofocus mode has been set via the AF switch SWAF (step CS303). If the AF switch SWAF is ON (if YES at step CS303), the AFON flag is set to "1" (step CS305) and subsequently control proceeds to step 10 CS309. If the AF switch SWAF is not ON (if NO at step CS303), the AFON flag is set to "0" (step CS307), and subsequently control proceeds to step CS309. If the AF switch SWAF is not ON (if NO at step CS301), the AFON flag is set to "0" (step CS307), and subsequently control 15 proceeds to step CS309.

At step CS309 it is determined whether the electronic flash is in the middle of charging. If the electronic flash is in the middle of charging (if YES at step CS309), a flag PAUSE is set to "1" (step CS311), and subsequently 20 control proceeds to step CS315. The flag PAUSE is set to "1" when any high power operation which requires a large current is performed at present. The electric flash charging operation corresponds to a high power operation in the present embodiment of the SLR camera system. 25 Therefore, the flag PAUSE is set to "1" when the electronic

flash is in the middle of charging. When the flag PAUSE is "1", the photographing lens 200 suspends all high power operations thereof. The film-winding operation and the shutter charge operation are also high power operations 5 performed in the present embodiment of the SLR camera system.

At step CS315 it is determined whether the photometering switch SWS is ON. If the photometering switch SWS is ON (if YES at CS315), the power hold flag PH is set to "1" (step CS321), and subsequently control 10 proceeds to step CS323. If the photometering switch SWS is not ON (if NO at CS315), it is determined whether the power hold timer has expired (step CS317). If the power hold timer has expired (if YES at step CS317), the power 15 hold flag PH is set to "0" (step CS319), and subsequently control proceeds to step CS323. If the power hold timer has not expired (if NO at step CS317), the power hold flag PH is set to "1" (step CS321), and subsequently control 20 proceeds to step CS323. The power hold timer measures the time from the moment the photometering switch SWS is turned OFF to the moment the body CPU 111 enters a sleep mode, and the power hold flag PH "1" or "0" indicates that the camera body 100 is in operation or in a sleep mode (a power saving mode), respectively.

25 At step CS323, it is determined whether the main

switch SWMAIN is ON. If the main switch SWMAIN is ON (if YES at step CS323), the SWMAIN flag is set to "1" (step CS325), and subsequently control returns. If the main switch SWMAIN is not ON (if NO at step CS323), the SWMAIN 5 flag is set to "0" (step CS327), and subsequently control returns.

[NEW-TYPE COMMUNICATION (LENS CPU COMMUNICATION)]

Timing charts in the new-type communication between the lens CPU 211 and the body CPU 111 are shown in Figures 10 18, 19A, 19B, 21A and 21B. In the new-type communication, the fifth contacts 104e and 204e (Fmax1/ Inverse-FBL), and the sixth contacts 104f and 204f (Fmax2/ Inverse-FLB), are used to serve as handshake connectors/lines (see Figures 19A and 19B). The level of each of the fifth contact 104e (Fmax1/ Inverse-FBL) and the sixth contact 15 104f (Fmax2/ Inverse-FLB) is pulled up by the body CPU 111 so that the fifth contact 104e (Fmax1/ Inverse-FBL) and the sixth contact 104f (Fmax2/ Inverse-FLB) cannot short circuit when the new type of photographing lens 200 20 is mounted to or dismounted from the camera body 100 (see Figures 19A and 19B).

[NEW-TYPE COMMUNICATION SETTING REQUEST PROCESS]

The new-type communication setting request process performed at step CS105 will be hereinafter discussed in 25 detail with reference to the flow chart shown in Figure

9.

In the new-type communication setting request process, firstly it is determined whether the new-type flag is "1", i.e., whether the photographing lens 200 currently mounted to the camera body 100 is a new type of photographing lens (step CS221). If the new-type flag is not "1" (if NO at step CS221), control returns since the currently mounted photographing lens 200 is not a new type of photographing lens. If the new-type flag is "1" (if YES at step CS221), operations at and after steps CS223 in the new-type communication setting request process are performed since the photographing lens 200 currently mounted to the camera body 100 is a new type of photographing lens 200 which allows new-type communication.

At step CS223 it is determined whether the second power flag VpzONCPU is "1". If the second power flag VpzONCPU is "1" (if YES at step CS223), the power contact 105 (VPZ) is turned ON, namely, power is supplied to the power contact 105 (VPZ) (step CS225). Subsequently, control proceeds to step CS227. On the other hand, if the second power flag VpzONCPU is not "1" (if NO at step CS223), control skips step CS225 to proceed straight from step CS221 to step CS225, so that no power is supplied to the power contact 105 (VPZ).

At step CS227, the level of the fifth contact 104e (Fmax1/ Inverse-FBL) is made to fall to a low level, and subsequently it is determined whether the level of the sixth contact 104f (Fmax2/ Inverse-FLB) is a low level (step CS229). The operation at step CS229 is repeated as long as the level of the sixth contact 104f (Fmax2/ Inverse-FLB) is a high level. If it is determined at step CS229 that the level of the sixth contact 104f (Fmax2/ Inverse-FLB) is a low level (if YES at step CS229), the level of the fifth contact 104e (Fmax1/ Inverse-FBL) is raised to a high level (step CS231), and subsequently it is determined whether the level of the sixth contact 104f (Fmax2/ Inverse-FLB) is a high level (step CS233). The operation at step CS233 is repeated as long as the level of the sixth contact 104f (Fmax2/ Inverse-FLB) is a low level. If it is determined at step CS233 that the level of the sixth contact 104f (Fmax2/ Inverse-FLB) is a high level (if YES at step CS233), control returns, i.e., control proceeds to step CS107.

20 [IMAGE-SHAKE COMPENSATION DATA SETTING PROCESS]

The image-shake compensation data setting process, which is performed at step CS137 on condition that the photographing lens 200 mounted to the camera body 100 is of a type which incorporates an image-shake compensation device, will be hereinafter discussed in detail with

reference to the flow chart shown in Figure 11. Figure 12A shows fundamental elements of a control system of an embodiment (first embodiment) of the photographing lens 200 which incorporates an image-shake compensation device. Figure 12B shows a conceptual diagram of a compensation lens (an image-stabilizing optical system) LC of the image-shake compensation device. The image-shake compensation device includes a pair of sensors, i.e., an X-direction angular speed sensor (horizontal-vibration sensor) 251 and a Y-direction angular speed sensor (vertical-vibration sensor) 252, for determining magnitude and direction of the vibration of the photographing lens 200 due to hand movement. If a state where the photographing lens 200 is properly mounted to the camera body 100 and normally held in a horizontal position is considered as a reference state, the X-direction angular speed sensor 251 senses the angular speed of the photographing lens 200 in the horizontal direction of the optical axis thereof (in the X-direction about the Y-axis), while the Y-direction angular speed sensor senses the angular speed of the photographing lens 200 in the horizontal direction of the optical axis thereof (in the Y-direction about the X-axis) wherein an intersection point of the optical axis of the photographing lens 200 and the picture plane

defines the intersection point of the X-axis and the Y-axis. Each of the vertical and horizontal vibration sensors can be a conventional gyro sensor. The vertical-vibration sensor exclusively senses the shake 5 of the photographing lens 200 in the vertical direction, while the horizontal-vibration sensor exclusively senses the shake of the photographing lens 200 in the horizontal direction.

The image-shake compensation device of the photographing lens 200 is provided with the compensation lens LC (see Figure 12B), and operates to compensate the shaking of the object image on the picture plane by driving the compensation lens LC in the X-direction and the Y-direction with an X-motor 254 and a Y-motor 257, 15 respectively, in a plane perpendicular to the optical axis of the photographing lens 200. The position of the compensation lens LC is sensed by the number of pulses output from each of an X-direction photo-interrupter 255 and a Y-direction photo-interrupter 258 when the 20 compensation lens LC is driven, wherein a position where the optical axis of the compensation lens LC coincides with the optical axis of the photographing lens 200 is regarded as a reference position. Rotation of each of the X-motor 254 and the Y-motor 257 is controlled by the 25 lens CPU 211 via an X-motor driver 253 and a Y-motor driver

256, respectively.

Note that the X-direction angular speed sensor 251, the Y-direction angular speed sensor 252, the X-motor driver 253, the Y-motor driver 256, the X-motor 254, the 5 Y-motor 257, X-direction photo-interrupter 255, the Y-direction photo-interrupter 258, and the compensation lens LC collectively constitute the image-shake compensation device.

The lens CPU 211 serves as a controller and an 10 arithmetic processing unit for the image-shake compensation device. The lens CPU 211 starts operating immediately after the image-shake compensation switch SW1 is turned ON to determine the direction of driving of the compensation lens LC and the amount of movement 15 (speed) thereof to drive the X-motor 254 and the Y-motor 257.

In the image-shake compensation data setting process shown in Figure 11, firstly, it is determined whether the main switch flag SWMAIN has changed from "0" to "1" (step 20 CS401). If the main switch flag SWMAIN has changed from "0" to "1" (if YES at step CS401), command 70 is transmitted to the photographing lens 200 to receive data therefrom (step CS403). Subsequently, the lens CPU 211 waits for an initialize flag Init "0" to be transmitted 25 from the camera body 100 (step CS405). Namely, it is

determined at step CS405 whether the initialize flag Init is "0".

Command 70 is data which is transmitted from the camera body 100 to the photographing lens 200 to make the 5 photographing lens 200 send the status thereof (status data) to the camera body 100. In the present embodiment of the SLR camera system, the status data which is transmitted from the photographing lens 200 to the camera body 100, immediately after the photographing lens 200 receives command 70, is represented by one-byte data, wherein the 7th bit thereof is defined as the initialize flag Init. The 0th bit of the one-byte data is defined as an identification bit which indicates whether an AF switch is ON or OFF. The 1st bit of the one-byte data 15 is defined as an identification bit which indicates whether the diaphragm is set automatically or manually. The 6th bit of the one-byte data is defined as an identification bit which indicates whether any function of the photographing lens 200 is in operation or not. 20 Each of the second through fifth bits is undefined. Therefore, each of the second through fifth bits can be defined as a specific identification bit if any new function is added to the photographing lens 200. It should be noted that 0 bit or no data indicates negation.

25 The initialize flag Init is changed from "1" to "0"

and output from the photographing lens 200 to the camera body 100 when an operation at step LS117 or LS125, in which the compensation lens LC is driven to return to the initial position thereof where the optical axis of the compensation lens coincides with the optical axis of the photographing lens 200, is completed. If it is determined at step CS405 that the initialize flag Init is "0", control proceeds to step CS407. If it is determined at step CS401 that the main switch flag SWMAIN has not changed from "0" to "1" (if NO at step CS401), control proceeds straight from step CS401 to CS407.

At step CS407, it is determined whether the main switch flag SWMAIN has changed from "1" to "0". If the main switch flag SWMAIN has changed from "1" to "0" (if YES at step CS407), this means that the main switch SWMAIN has turned from ON to OFF, so that subsequently command 70 is transmitted to the photographing lens 200 to receive data therefrom (step CS409). Subsequently, the lens CPU 211 waits for the initialize flag Init "0" to be transmitted from the camera body 100 (step CS411). Namely, it is determined at step CS411 whether the initialize flag Init is "0". Control returns to step CS409 if the initialize flag Init is not "0".

If it is determined at step CS411 that the initialize flag Init is "0", control proceeds to step CS413. If it

is determined at step CS407 that the main switch flag SWMAIN has not changed from "1" to "0" (if NO at step CS407), control proceeds straight from step CS407 to CS413.

If it is determined at step CS413 that the power hold flag PH has changed from "1" to "0" (if YES at step CS413),
5 command 70 is transmitted to the photographing lens 200 to receive data therefrom (step CS415). Subsequently, the lens CPU 211 waits for the initialize flag Init "0" to be transmitted from the camera body 100 (step CS417).
10 Namely, it is determined at step CS417 whether the initialize flag Init is "0". Control returns to step CS415 if the initialize flag Init is not "0". If it is determined at step CS417 that the initialize flag Init is "0", control proceeds to step CS419. If it is
15 determined at step CS413 that the power hold flag PH has not changed from "1" to "0" (if NO at step CS413), control proceeds straight from step CS413 to CS419.

At step CS419 it is determined whether the image-shake compensation switch SW1 has been turned from ON to OFF.
20 If the image-shake compensation switch SW1 has been turned from ON to OFF (if YES at step CS419), command D1 (individual function data) for turning OFF the image-shake compensation function of the photographing lens 200 is transmitted thereto (step CS421), and subsequently
25 control proceeds to step CS423. Upon receiving command

D1, the photographing lens 200 completes the image-shake compensation operation. If the image-shake compensation switch SW1 has not been turned from ON to OFF (if NO at step CS419), control skips CS421 to proceed straight from 5 step CS419 to step CS423. At step CS423 it is determined whether the image-shake compensation switch SW1 has been turned from OFF to ON. If the image-shake compensation switch SW1 has been turned from OFF to ON (if YES at step CS423), command D2 (individual function data) for turning 10 ON the image-shake compensation function of the photographing lens 200 is transmitted thereto (step CS425), and subsequently control returns. If the image-shake compensation switch SW1 has not been turned from OFF to ON (if NO at step CS423), control skips CS425 15 and returns. Upon receiving command D2, the photographing lens 200 starts the image-shake compensation operation.

Fundamental operations and processes performed by the lens CPU 211 of the photographing lens 200 that 20 incorporates the image-shake compensation device will be hereinafter discussed in detail with reference to the flow charts shown in Figures 13 through 17. Figure 13 shows a flow chart for the main process of the photographing lens 200 which is performed by the lens CPU 211. Control 25 enters the main process immediately after the lens CPU

211 is supplied with power via the operation at step CS225, at which power is supplied to the power contact 105 (VPZ).

In the main process shown in Figure 13, firstly the lens CPU 211 initializes internal RAM and ports thereof (step LS101). Subsequently, a new-type communication setting process ("new-type communication setting process" shown in Figure 14) is performed (step LS103). In this process, a 1ms-timer interrupt (see Figure 15) and an interrupt via the port (inverse) INT of the lens CPU 211 (see Figure 16) are enabled to receive an interrupt from the camera body 100 to thereby make the new-type communication possible between the new type of photographing lens (photographing lens 200) and the camera body 100.

Subsequently, it is determined whether a sleep flag which is set to "1" at step LS433 or LS 437 is "1" (step LS105). If the sleep flag is "1" (if YES at step LS105), the lens CPU 211 stops operations of internal devices of the photographing lens 200 such as the lens motor (step LS107), the sleep flag is set to "0" (step LS109), and the lens CPU 211 enters the sleep mode (step LS111). The lens CPU 211 "wakes up" upon receiving an interrupt signal via the port (inverse) INT thereof.

If it is determined at step CS105 that the sleep flag is not "1" (if NO at step LS105), it is determined whether

a compensation lens reset flag is "1" (step LS113). If the compensation lens reset flag is "1" (if YES at step LS113), the initialize flag Init is set to "1" (step LS115). Subsequently, a resetting operation is performed (step 5 LS117). In the resetting operation, the X-motor 254 and Y-motor 257 are driven to move the compensation lens LC to firstly a predetermined mechanical extremity (reference point) in the range of movement of the compensation lens LC, and subsequently the initial 10 position (central position) thereof where the optical axis of the compensation lens LC coincides with the optical axis of the photographing lens 200. After the resetting operation is performed, the compensation lens reset flag and the initialize flag are set to "0" (step 15 LS119), and control proceeds to step LS121. According to this resetting operation, the absolute position of the compensation lens LC is secured, and accordingly the compensation lens LC can be positioned precisely at the initial position (central position) thereof.

20 If it is determined at step LS113 that the compensation lens reset flag is not "1", it is determined whether a compensation lens center flag is "1" (step LS121). If the compensation lens center flag is not "1" (if NO at step LS121), control returns to step LS105. If 25 the compensation lens center flag is "1" (if YES at step

LS121), the initialize flag Init is set to "1" (step LS123). Subsequently, a centering operation is performed in which the X-motor 254 and Y-motor 257 are driven to move the compensation lens LC to the initial position (central position) where the optical axis of the compensation lens LC coincides with the optical axis of the photographing lens 200 (step LS125). Subsequently, the compensation lens center flag and the initialize flag are set to "0" (step LS127), and control returns to step LS105.

The new-type communication setting process performed at step LS103 will be hereinafter discussed in detail with reference to the flow chart shown in Figure 14. In the new-type communication setting process, firstly it is determined whether the level of the fifth contact 204e (Fmax1/ Inverse-FBL) is a low level (step LS221). If the level of the fifth contact 204e (Fmax1/ Inverse-FBL) is not a low level (if NO step LS221), the operation at step LS221 is performed again, so that the operation at step LS221 is repeated until the level of the fifth contact 204e (Fmax1/ Inverse-FBL) falls to a low level. If the level of the fifth contact 204e (Fmax1/ Inverse-FBL) is a low level (if YES step LS221), the sixth contact 204f (Fmax2/ Inverse-FLB) is made to fall to a low level (step LS223), and subsequently a communication setting process is performed (step LS225). The

communication setting process includes a setting process for serial communication, and an interrupt enabling process via the port (inverse) INT of the lens CPU 211.

Upon completion of the communication setting process at step LS225, it is determined whether the level of the fifth contact 204e (Fmax1/ Inverse-FBL) is a high level (step LS227). If the level of the fifth contact 204e (Fmax1/ Inverse-FBL) is not a high level (if NO step LS227), the operation at step LS227 is performed again, so that the operation at step LS227 is repeated until the level of the fifth contact 204e (Fmax1/ Inverse-FBL) rises to a high level. If the level of the fifth contact 204e (Fmax1/ Inverse-FBL) is a high level (if YES step LS227), the sixth contact 204f (Fmax2/ Inverse-FLB) is raised to a high level (step LS229), and subsequently control returns.

A 1ms-timer interrupt process for the image-shake compensation operation will be hereinafter discussed in detail with reference to the flow chart shown in Figure 15. The 1ms-timer interrupt process starts each time a 1ms hard timer expires during operation of the lens CPU 211. In the 1ms-timer interrupt process, the lens CPU 211 inputs an angular speed signal from each of the X-direction angular speed sensor 251 and the Y-direction angular speed sensor 252 to detect the vibration of the

photographing lens 200 due to hand movement, and subsequently determines the direction of driving of the compensation lens LC and the amount of movement (speed) thereof to drive the X-motor 254 and the Y-motor 257 to 5 move the compensation lens LC by the determined amount of movement in the determined driving direction.

In the 1ms-timer interrupt process, firstly it is determined whether a compensation function OFF flag is "1" (step LS301). If the compensation function OFF flag 10 is "1" (if YES step LS301), a compensation work flag is set to "0" (step LS303) and control returns. The compensation work flag "1" or "0" indicates that the image-shake compensation device operates or does not operate, respectively.

If the compensation function OFF flag is "0" (if NO step LS301), it is determined whether a compensation ON flag is "0" (step LS305). If the compensation ON flag is "0" (if YES step LS305), this means that the image-shake compensation operation is not performed, so that the 20 compensation work flag is set to "0" (step LS303), and control returns.

If the compensation ON flag is "1" (if NO step LS305), the compensation work flag is set to "1", and subsequently a vibration detection process is performed (step LS309).
25 In the vibration detection process, the lens CPU inputs

an angular speed signal from each of the X-direction angular speed sensor 251 and the Y-direction angular speed sensor 252 to detect the vibration of the photographing lens 200, and subsequently determines the direction of driving of the compensation lens LC and the amount of movement thereof.

After the vibration detection process at step LS309 is performed, it is determined whether a drive ON flag is "0" (step LS311). If the drive ON flag is not "0" (if NO at step LS311), the X-motor 254 and the Y-motor 257 are driven to move the compensation lens LC by the amount of movement in the determined driving direction that are determined at step LS309 (step LS315), and subsequently control returns. If the drive ON flag is "0" (if YES at step LS311), the driving of each of the X-motor 254 and the Y-motor 257 is stopped forcefully (step LS313), and control returns.

An inverse-INT interrupt process will be hereinafter discussed with reference to the flow chart shown in Figures 16 and 17. The inverse-INT interrupt process starts immediately after the level of the fifth contact 204e (Fmax1/ Inverse-FBL) falls to a low level to thereby cause the port (inverse) INT of the lens CPU 211 to fall to a low level.

In the inverse-INT interrupt process, firstly at

least one command is received from the camera body 100 via the new-type communication (step LS401). Subsequently, it is determined whether at least one of commands 70, 71 and 72 was received at step LS401 (step 5 LS403). If at least one of commands 70, 71 and 72 was received at step LS401 (if YES at step LS403), a lens data transmitting process (8-bit data transmitting process) is performed via the new-type communication (step LS405), and control proceeds to step LS407. If none of commands 10 70, 71 and 72 was received at step LS401 (if NO at step LS403), control proceeds straight from step LS403 to step LS407.

At step LS407, it is determined whether at least one of commands B0 and B1 was received at step LS401. If 15 neither of commands B0 and B1 was received at step LS401 (if NO at step LS407), control proceeds to step LS431. If at least one of commands B0 and B1 was received at step LS401 (if YES at step LS407), a body data receiving process is performed via the new-type communication (step LS409). 20 Subsequently, it is determined whether the main switch flag SWMAIN has changed from "0" to "1" (step LS411), whether the main switch flag SWMAIN has changed from "1" to "0" (step LS415), whether the power hold flag PH is "1" (step LS419), and whether the power hold flag PH has 25 changed from "1" to "0" (step LS423).

If it is determined at step LS411 that the main switch flag SWMAIN has changed from "0" to "1" (if YES at step LS411), the compensation lens reset flag is set to "1" (step LS413), and control proceeds to step LS415.

- 5 If it is determined at step LS415 the main switch flag SWMAIN has changed from "1" to "0" (if YES at step LS415), the compensation lens center flag is set to "1" (step LS417), and control proceeds to step LS419. If it is determined at step LS419 that the power hold flag PH is "1" (if YES at step LS419), the compensation ON flag is set to "1" (step LS421), and control proceeds to step LS423. If it is determined at step LS423 that the power hold flag PH has changed from "1" to "0" (if YES at step LS423), the compensation ON flag is set to "0" and the compensation lens center flag is set to "1" (step LS424), and control proceeds to step LS425. If it is determined "NO" at all of steps LS411, LS415, LS419 and LS423, control proceeds from step LS411 to step LS425 with none of the operations at steps LS413, LS417, LS421 and LS424 being performed.

- 20 At step LS425, it is determined whether the flag PAUSE is "1". If the flag PAUSE is "1" (if YES at step LS425), the drive ON flag is set to "0" (step LS427), and control proceeds to step LS431. If the flag PAUSE is "0" (if NO at step LS425), the drive ON flag is set to "1" (step LS429), and control proceeds to step LS431. The

flag PAUSE is set to "1" when any high power operation which requires a large current is currently performed. In the present embodiment of the SLR camera system, the flag PAUSE is set to "1" when the electronic flash is in 5 the middle of charging (step CS311). Subsequently, the lens CPU 211 sets the drive ON flag to "0" upon receipt of the flag PAUSE "1" (step LS427), and control proceeds from step LS311 to step LS313 to stop the driving of each of the X-motor 254 and the Y-motor 257 forcefully in the 10 1ms-timer interrupt process shown in Figure 15. However, the X-direction angular speed sensor 251 and the Y-direction angular speed sensor 252 continue to operate.

It is determined at step LS431 whether at least one of commands 71 and B1 was received at step LS401. If 15 at least one of commands 71 and B1 was received at step LS401 (if YES at step LS431), the sleep flag is set to "1" (step LS433), and control proceeds to step LS435. If neither of commands 71 and B1 was received at step LS401 (if NO at step LS431), control proceeds from step LS431 20 to step LS435. If the sleep flag is set to "1", control proceeds from step LS105 to step LS107 so that the lens CPU 211 enters the sleep mode in the main process shown in Figure 13.

Command 71 is data which is transmitted from the 25 camera body 100 to the photographing lens 200 to make the

photographing lens 200 send the status thereof to the camera body 100, and also to make the lens CPU 211 enter a sleep mode together with the body CPU 111.

At step LS435, it is determined whether command D0 was received at step LS401. If command D0 was received at step LS401 (if YES at step LS435), the sleep flag is set to "1" (step LS437), and control proceeds to step LS439. If command D0 was not received at step LS401 (if NO at step LS435), control proceeds straight from step LS435 to step LS439.

At step LS439, it is determined whether command D1 was received at step LS401. If command D1 was received at step LS401 (if YES at step LS439), the compensation function OFF flag is set to "1" (step LS441), and control proceeds to step LS443. If command D1 was not received at step LS401 (if NO at step LS439), control proceeds straight from step LS439 to step LS443.

At step LS443 it is determined whether command D2 was received at step LS401. If command D2 was received at step LS401 (if YES at step LS443), the compensation function OFF flag is set to "0" (step LS445), and control proceeds to step LS447. If command D2 was not received at step LS401 (if NO at step LS443), control proceeds straight from step LS443 to step LS447.

At step LS447 it is determined whether command was

received at step LS401. If command 7F was received at step LS401 (if YES at step LS447), the lens CPU 211 performs a dummy data communication process (step LS449) and control returns. If command 7F was not received at 5 step LS401 (if NO at step LS447), control returns.

Command 7F is data which is transmitted from the camera body 100 to the photographing lens 200 to make the rear converter 300 output data therefrom, if the rear converter 300 is mounted between the camera body 100 and 10 the photographing lens 200. The dummy data communication process at step LS449 is performed to allocate communication channel for the body CPU 111 so that the body CPU 111 can receive data output from the rear converter 300.

15 Fundamental structures and processes of an embodiment of the photographing lens 200 which incorporates an image-shake compensation device have been described above. Another embodiment (second embodiment) of the photographing lens 200 which 20 incorporates a lens AF system will be hereinafter discussed with reference to Figures 22 through 26. It should be noted that elements and processes/processes in the second embodiment of the photographing lens 200 which are similar to those in the first embodiment of the 25 photographing lens 200 shown in Figures 12 through 17 are

respectively designated by similar reference numerals and step numbers.

Figure 22 is a block diagram of fundamental elements of a communication/control system of the second embodiment of the photographing lens 200 which incorporates a lens AF system. The second embodiment of the photographing lens 200 is provided with an AF motor driver 261, an AF motor (lens motor) 262 and a photo-interrupter 263. The lens CPU 211 drives the AF motor 262 via the AF motor driver 261 in accordance with data on the driving-amount of the AF motor 262 and the driving direction thereof that is received from the body CPU 111 to move a focusing lens group Lf along the optical axis thereof to an axial position thereon at which an in-focus state is obtained. The amount of movement of the focusing lens group Lf is detected by counting the number of pulses output from the photo-interrupter 263.

Figure 23 shows a flow chart for the main process of the second embodiment of the photographing lens 200 which incorporates a lens AF system. Control enters the main process immediately after the lens CPU 211 is supplied with power via the operation at step CS225, at which power is supplied to the power contact 105 (VPZ).

In the main process shown in Figure 23, firstly the lens CPU 211 initializes internal RAM and ports thereof

(step LS101). Subsequently, the new-type communication setting process ("new-type communication setting process" shown in Figure 14) is performed (step LS103). In this process, a 1ms-timer interrupt (see Figure 24) 5 and an interrupt via the port (inverse) INT of the lens CPU 211 (see Figure 25) are enabled to receive an interrupt from the camera body 100 to thereby make the new-type communication possible between the photographing lens 200 and the camera body 100.

10 Subsequently, it is determined whether a sleep flag which is set to "1" at step LS433 or LS437 is "1" (step LS105). If the sleep flag is "1" (if YES at step LS105), the lens CPU 211 stops internal devices of the photographing lens 200 such as the AF motor 262 and the 15 photo-interrupter 263 (step LS107), the sleep flag is set to "0" (step LS109), and the lens CPU 211 enters the sleep mode (step LS111). The lens CPU 211 wakes up on receiving an interrupt signal via the port (inverse) INT thereof.

If it is determined at step CS105 that the sleep flag 20 is not "1" (if NO at step LS105), the operation at step LS105 is repeated. The new-type communication setting process shown in Figure 14, a 1ms-timer interrupt process shown in Figure 24, and an inverse-INT interrupt process shown in Figure 25 are performed during the time the 25 operation at step LS105 is repeated.

The new-type communication setting process performed at step LS103 shown in Figure 23 is identical to that shown in Figure 14, and accordingly further description of the new-type communication setting process performed at step LS103 shown in Figure 23 is omitted.

The 1ms-timer interrupt process which is repeated at regular intervals when the lens CPU 211 is in operation, in the second embodiment of the photographing lens 200, will be hereinafter discussed with reference to the flow chart shown in Figure 24. This 1ms-timer interrupt process starts each time a 1ms hard timer expires during operation of the lens CPU 211 to control operation of the AF motor 262.

In the 1ms-timer interrupt process, firstly it is determined whether an AF function ON flag is "0" (step LS331). If the AF function ON flag is "0" (if YES step LS331), a lens AF process is not performed, wherein an AF work flag is set to "0" (step LS333), and control returns. The AF work flag "1" or "0" indicates that the lens AF process operates or does not operate, respectively.

If the AF function ON flag is not "0" (if NO step LS331), it is determined whether a drive end flag is "1" (step LS335). If the drive end flag is "1" (if YES at

step LS335), this means that the driving of the AF motor 262 has been completed, so that the AF work flag is set to "0" (step LS333) and control returns.

If the drive end flag is not "1" (if NO at step LS335),
5 it is determined whether a drive ON flag is "0" (step LS337). If the drive ON flag is "0" (if YES at step LS337), the AF motor 262 is stopped forcefully (step LS339), and control returns.

If the drive ON flag is not "0" (if NO at step LS337),
10 the AF work flag is set to "1" (step LS341), and subsequently the AF motor 343 is started (driven) (step LS343). Subsequently, it is determined whether the driving of the AF motor 343 is completed (step LS345). If the driving of the AF motor 343 is completed (if YES
15 at step LS345), the drive end flag is set to "1" (step LS347), and control returns. If the driving of the AF motor 343 has not been completed (if NO at step LS345), control returns.

An inverse-INT interrupt process in the second
20 embodiment of the photographing lens 200 will be hereinafter discussed with reference to the flow chart shown in Figures 25 and 26. The inverse-INT interrupt process starts immediately after the level of the fifth contact 204e (Fmax1/ Inverse-FBL) falls to a low level
25 to thereby cause the port (inverse) INT of the lens CPU

211 to fall to a low level.

In the inverse-INT interrupt process, firstly at least one command is received from the camera body 100 via the new-type communication (step LS401).

5 Subsequently, it is determined whether at least one of commands 70, 71 and 72 was received at step LS401 (step LS403). If at least one of commands 70, 71 and 72 was received at step LS401 (if YES at step LS403), a lens data transmitting process (8-bit data transmitting process) 10 is performed via the new-type communication, and control proceeds to step LS407. If none of commands 70, 71 and 72 was received at step LS401 (if NO at step LS403), control proceeds straight from step LS403 to step LS407.

At step LS407, it is determined whether at least one 15 of commands B0 and B1 was received at step LS401. If neither of commands B0 and B1 was received at step LS401 (if NO at step LS407), control proceeds to step LS461. If at least one of commands B0 and B1 was received at step LS401 (if YES at step LS407), a body data receiving process 20 is performed via the new-type communication (step LS409). Subsequently, it is determined whether the AF ON flag is "1" (step LS451). If the AF ON flag is "1", (if YES at step LS451), the AF function ON flag is set to "1" (step LS453), and control proceeds to step LS455. If the AFON 25 flag is not "1", (if NO at step LS451), the AF function

ON flag is set to "0" (step LS454), and control proceeds to step LS455.

At step LS455 it is determined whether the release stage indicator RLS is "2". The release stage indicator RLS is a two-bit data which is set to "0", "1", "2" or "3" by the body CPU 111. The release stage indicator RLS "1" indicates a stage at which the quick return mirror is moving toward the retracted position thereof after the release switch SWR has been turned ON. The release stage indicator RLS "2" indicates a stage at which a film frame is under exposure after the quick return mirror has moved up to the retracted position thereof. The release stage indicator RLS "3" indicates a stage at which the camera body 100 is in a stage at which film is advanced after the exposure operation has been completed. The release stage indicator RLS "0" indicates any other stage of the camera body 100. If the release stage indicator RLS is "2" (if YES at step LS455), this means that a film frame is under exposure after the quick return mirror has moved up to the retracted position thereof, so that the drive ON flag is set to "0" (step LS457), and control proceeds to step LS461. If the release stage indicator RLS is not "2" (if NO at step LS455), the drive ON flag is set to "1" (step LS459) and control proceeds to step LS461.

At step LS461, it is determined whether command B2

was received at step LS401. If command B2 was received at step LS401 (if YES at step LS461), lens driving amount data is received from the body CPU 111 (step LS463). Subsequently, this received lens driving amount data is 5 set (step LS465), and the drive end flag is set to "0" (step LS467). Subsequently, control proceeds to step LS431. If command B2 was not received at step LS401 (if NO at step LS461), control proceeds to step LS431.

It is determined at step LS431 whether at least one 10 of commands 71 and B1 was received at step LS401. If at least one of commands 71 and B1 was received at step LS401 (if YES at step LS431), the sleep flag is set to "1" (step LS433) and control proceeds to step LS435. If neither of commands 71 and B1 was received at step LS401 15 (if NO at step LS431), control proceeds from step LS431 to step LS435. If the sleep flag is set to "1", control proceeds from step LS105 to step LS107, in the main process shown in Figure 13, so that the lens CPU 211 enters the sleep mode.

At step LS435, it is determined whether command D0 20 was received at step LS401. If command D0 was received at step LS401 (if YES at step LS435), the sleep flag is set to "1" (step LS437) and control proceeds to step LS469. If command D0 was not received at step LS401 (if NO at 25 step LS435), control proceeds straight from step LS435

to step LS469.

At step LS469, it is determined whether command D3 was received at step LS401. If command D3 was received at step LS401 (if YES at step LS469), the drive ON flag 5 is set to "0" (step LS471), and control proceeds to step LS473. If command D3 was not received at step LS401 (if NO at step LS469), control proceeds straight from step LS469 to step LS473.

At step LS473, it is determined whether command D4 10 was received at step LS401. If command D4 was received at step LS401 (if YES at step LS473), the drive ON flag is set to "1" (step LS475), and control proceeds to step LS477. If command D4 was not received at step LS401 (if NO at step LS473), control proceeds straight from step 15 LS473 to step LS477.

At step LS477 it is determined whether command 7F was received at step LS401. If command 7F was received at step LS401 (if YES at step LS447), the lens CPU 211 performs the dummy data communication process (step 20 LS479), and control returns. If command 7F was not received at step LS401 (if NO at step LS477), control returns. At this point, command 7F is issued for data communication between the lens CPU 211 and a CPU (controller/rear converter controller) 311 of the rear 25 converter 300. In a state where the rear converter 300

is mounted between the camera body 100 and the photographing lens 200, the CPU 311 of the rear converter 300 recognizes command 7F as a command for the rear converter 300 upon receipt of command 7F, and subsequently 5 sends rear converter communication data (new-type communication data) to the body CPU 111.

Figure 27 is a schematic block diagram of fundamental elements of control systems of the photographing lens 200, the rear converter 300, and the 10 camera body 100 of an embodiment of the SLR camera system according to the present invention, wherein the rear converter 300 is mounted between the camera body 100 and the photographing lens 200. The rear converter 300 is provided with the CPU 311 serving as a controller which 15 controls functions of the rear converter 300, and the ROM 321 serving as a storing device in which fundamental data on the rear converter 300 are stored. As shown in Figure 27, each of the CPU 311 and the ROM 321 of the rear converter 300 is provided with I/O ports similar to those 20 of the lens CPU 211 and the lens ROM 221. The rear converter 300 can either serve as a teleconverter or wide converter, being provided with a variable power lens group (not shown).

The rear converter 300 is provided with a rear mount 25 303 which is mounted to the mount 103 of the camera body

100, and a front mount 3031 to which the lens mount 203
of the photographing lens 200 is mounted. The rear mount
303 is provided thereon with six contacts
(relay/communication contacts) which come in contact
5 with the six contacts 104a through 104f provided on the
mount 103 of the camera body 100, respectively, while the
front mount 3031 is provided thereon with another six
contacts (relay/communication contacts) which come in
contact with the six contacts 204a through 204f provided
10 on the lens mount 203 of the photographing lens 200.

The six contacts formed on the rear mount 303 of the
rear converter 300 include a first contact 304a (Fmin1/
Inverse-SCKL), a second contact 304b (Fmin2/ DATAL), a
third contact 304c (Fmin3/ RESL), a fourth contact 304d
15 (CONTL), a fifth contact 304e (Fmax1/ Inverse-FBL), and
a sixth contact 304f (Fmax2/ Inverse-FLB). The six
contacts formed on the front mount 3031 of the rear
converter 300 include a first contact 304a1 (Fmin1/
Inverse-SCKL), a second contact 304b1 (Fmin2/ DATAL), a
20 third contact 304c1 (Fmin3/ RESL), a fourth contact 304d1
(CONTL), a fifth contact 304e1 (Fmax1/ Inverse-FBL), and
a sixth contact 304f1 (Fmax2/ Inverse-FLB) which are
connected with the first contact 304a (Fmin1/
Inverse-SCKL), the second contact 304b (Fmin2/ DATAL),
25 the third contact 304c (Fmin3/ RESL), the fourth contact

304d (CONTL), the fifth contact 304e (Fmax1/ Inverse-FBL), and the sixth contact 304f (Fmax2/ Inverse-FLB) on the rear mount 303, respectively. Accordingly, the rear six contacts 304a through 304f and the front six contacts 5 304a1 through 304f1 of the rear converter 300 serve as a group of relay channels via which the group of communication/control contacts 104 (104a through 104f) on the mount 103 of the camera body 100 are electrically connected with the group of communication/control 10 contacts 204 (204a through 204f) on the lens mount 203 of the photographing lens 200, respectively, in a state where the rear converter 300 is properly mounted between the camera body 100 and the photographing lens 200.

The first contact 304a (Fmin1/ Inverse-SCKL) on the 15 rear mount 303 and the first contact 304a1 (Fmin1/ Inverse-SCKL) on the front mount 3031 are connected to a port Inverse-SCK of each of the CPU 311 and the lens ROM 321. The second contact 304b (Fmin2/ DATA1) on the rear mount 303 and the second contact 304b1 (Fmin2/ DATA1) 20 on the front mount 3031 are connected to a port DATA of each of the CPU 311 and the lens ROM 321. The third contact 304c (Fmin3/ RESL) on the rear mount 303 and the third contact 304c1 (Fmin3/ RESL) on the front mount 3031 are connected to a port RES of each of the CPU 311 and the 25 lens ROM 321. The fourth contact 304d (CONTL) on the rear

mount 303 and the fourth contact 304d1 (CONTL) on the front mount 3031 are connected to a port CONT of each of the CPU 311 and the lens ROM 321. The fifth contact 304e (Fmax1/ Inverse-FBL) on the rear mount 303 and the fifth 5 contact 304e1 (Fmax1/ Inverse-FBL) on the front mount 3031 are connected to a port (Fmax/ Inverse-FBL) of each of the CPU 311 and the lens ROM 321. The sixth contact 304f (Fmax2/ Inverse-FLB) on the rear mount 303 and the sixth contact 304f1 (Fmax2/ Inverse-FLB) on the front mount 3031 10 are not connected to any ports of each of the CPU 311 and the lens ROM 321.

The CPU 311 of the rear converter 300 is not provided with any port which corresponds to a connection port of the lens CPU 211 that is connected to the sixth contact 15 204f (Fmax2/ Inverse-FLB) on the lens mount 203. The lens CPU 211 starts communicating with the body CPU 111 immediately after the level of the fifth contacts 104e and 204e (Fmax1/ Inverse-FBL) fall to a low level, and a handshake operation is performed between the body CPU 20 111 and the lens CPU 211 via the sixth contacts 104f and 204f (Fmax2/ Inverse-FLB). While the handshake operation is performed, the CPU 311 sends out data (rear converter data) on the rear converter 300 via a port Fmax1 of the CPU 311 in synchronization with signals transmitted 25 through the fifth contacts 104e and 204e (Fmax1/

Inverse-FBL). For this reason, the CPU 311 is provided with no port which is connected to the sixth contacts 104f and 204f (Fmax2/ Inverse-FLB) which are used for the handshake performed between the body CPU 111 and the lens 5 CPU 211. The fifth contact 304e (Fmax1/ Inverse-FBL) on the rear mount 303 is connected to a first communication/control contact. The first communication/control contact is defined by the fifth contacts 104e and 204e.

10 The rear converter 300 is provided on the rear mount 303 thereof with a power contact 305, and is provided on the front mount 3031 thereof with a power contact 3051. The power contact 105 of the camera body 100 is connected with the power contact 205 of the photographing lens 200 15 via the two power contacts 305 and 3051 when the rear converter 300 is mounted between the camera body 100 and the photographing lens 200. Battery power can be supplied to the CPU 311 from the battery 113 via the power contacts 105 and 205. Likewise, battery power can be 20 supplied to the lens CPU 211 from the battery 113 via the power contacts 305 and 3051.

The rear converter 300 is characterized in that the CPU 311 thereof sends out data on the rear converter 300 on demand of the body CPU 111 while the handshake operation 25 is performed between the body CPU 111 and the lens CPU

211.

Fundamental operations of the embodiment of the SLR camera system with the rear converter 300 will be hereinafter discussed in detail with reference to the 5 timing charts shown in Figures 28 and 29 in combination with the flow charts shown in Figures 30, 31 and 32. Operations for communications between the camera body 100 and the photographing lens 200 are to the same as those in the above-described embodiment of the SLR camera system 10 wherein the rear converter 300 is not mounted between the camera body 100 and the photographing lens 200.

Immediately after the main switch SWMAIN is turned ON in a state where the rear converter 300 is properly mounted between the camera body 100 and the photographing 15 lens 200, as shown in Figure 27, the body CPU 111 supplies the first power to the fourth contact 104d (CONTL), and makes the level of the third contact 104c (Fmin3/ RESL) fall to a low level to communicate with the lens ROM 221, i.e., to perform the lens ROM communication (the old-type communication). At this time, the first power is 20 supplied to each of the CPU 311 and the ROM 321 of the rear converter 300 via the fourth contact 304d (CONTL), while the lens ROM 221 is set to change from a disable state to an enable state if the level of the third contact 25 304c (Fmin3/ RESL) falls to a low level.

Although this old-type communication is similar to the old-type communication performed with no rear converter being mounted between the camera body 100 and the photographing lens 200, the lens ROM 221 does not

5 output any data for the last few bytes when sending out the lens data on the photographing lens 200, rather, the ROM 321 of the rear converter 300 outputs data on the rear converter 300 (rear converter data) instead. The rear converter data that is output from the ROM 321 of the rear

10 converter 300 is, e.g., data on the type of the rear converter 300, so that the body CPU 111 recognizes the existence of the rear converter 300 upon receiving the rear converter data. Subsequently, if the body CPU 111 recognizes the rear converter 300 as a new type which is

15 compatible with the new-type lens communication, the body CPU 111 issues command 7F to the rear converter 300 to communicate with the CPU 311 of the rear converter 300 to receive the aforementioned rear converter communication data (new-type communication data) from

20 the CPU 311. Since the rear converter communication data is transmitted to the body CPU 111 under control of the CPU 311 of the rear converter 300, data values in the rear converter communication data can be varied in accordance with a variation in power of the rear converter 300, and/or

25 the rear converter communication data can be sent out with

additional information. This increases the degree of freedom in the development of the rear converter 300.

Figure 30 shows a flow chart for the main process of the rear converter 300 which is performed by the CPU 5 311 thereof. Control enters the main process shown in Figure 30 immediately after the level of the fifth contact 304e (Fmax1/ Inverse-FBL) falls to a low level and the level of the third contact 304c (Fmin3/ RESL) rises to a high level when the body CPU 111 supplies the first power 10 to the fourth contact 304d (CONTL). It should be noted that operations or processes having step numbers bearing a prefix "RS" are related to control/operation of the rear converter 300.

In the main process shown in Figure 30, firstly the 15 CPU 311 of the rear converter 300 initializes internal RAM and ports thereof (step RS101). Subsequently, a new-type communication setting process ("new-type communication setting process" shown in Figure 31) is performed (step RS103). In this process, an interrupt 20 via an interrupt port Fmax1 (inverse-INT) of the CPU 311 is enabled to receive an interrupt from the camera body 100 to thereby make the new-type communication possible between the camera body 100 and the rear converter 300.

Subsequently, it is determined whether a sleep flag 25 which is set to "1" by a command issued by the body CPU

111 is "1" (step RS105). If the sleep flag is "1" (if YES at step RS105), the CPU 311 performs a CPU stop operation to enter a power saving mode (step RS107), the sleep flag is set to "0" (step RS109), and the CPU 311 5 enters sleep mode (step RS111). The CPU 311 wakes upon receiving an interrupt signal via the interrupt port Fmax1 (inverse-INT) of the CPU 311.

If it is determined at step RS105 that the sleep flag is not "1" (if NO at step RS105), the operation at step 10 RS105 is repeated. The new-type communication setting process shown in Figure 31 is performed during the time the sleep flag (which is checked at step RS105) is "0".

The new-type communication setting process performed at step RS103 will be hereinafter discussed in 15 detail with reference to the flow chart shown in Figure 31. In the new-type communication setting process, firstly it is determined whether the level of the interrupt port Fmax1 (inverse-INT) is a low level (step RS201). If the level of the interrupt port Fmax1 20 (inverse-INT) is not a low level (if NO step RS201), the operation at step RS201 is performed again, so that the operation at step RS201 is repeated until the level of the interrupt port Fmax1 (inverse-INT) falls to a low level. If the level of the interrupt port Fmax1 25 (inverse-INT) falls to a low level (if YES step RS201),

a communication setting process is performed (step RS203). The communication setting process includes a setting process for serial communication, and an interrupt enabling process via the interrupt port Fmax1 (inverse-INT). Upon completion of the communication setting process at step RS203, it is determined whether the level of the interrupt port Fmax1 (inverse-INT) is a high level (step RS205). If the level of the interrupt port Fmax1 (inverse-INT) is not a high level (if NO step 5 RS205), the operation at step RS205 is performed again, so that the operation at step RS205 is repeated until the level of interrupt port Fmax1 (inverse-INT) rises to a high level. Control returns if the level of interrupt port Fmax1 (inverse-INT) rises to a high level (if YES 10 step RS205).

15

The inverse-INT interrupt process performed by the CPU 311 of the rear converter 300 will be hereinafter discussed with reference to the flow chart shown in Figure 32. The inverse-INT interrupt process shown in Figure 20 32 starts immediately after the level of the fifth contact 304e (Fmax1/ Inverse-FBL) falls to a low level to thereby cause the interrupt port Fmax1 (inverse-INT) of the CPU 311 to fall to a low level. In the inverse-INT interrupt process shown in Figure 32, the CPU 311 sends the rear converter data to the camera body 100 upon receipt of 25

command 7F.

In the inverse-INT interrupt process shown in Figure 32, firstly at least one command (8-bit data) is received from the camera body 100 via the new-type communication 5 (step RS301). Subsequently, it is determined whether at least one of commands 70, 71, 72, B0, B1, B2, D1, D2, D3 and D4 was received at step RS301 (step RS303). If at least one of these commands was received at step RS301 (if YES at step RS303), such a command or commands are 10 those which have been issued for the photographing lens 200, so that a dummy data communication process in which the CPU 311 receives unnecessary communication data (dummy data) for the rear converter 300 is performed (step RS305), and subsequently control proceeds to step RS307. 15 If none of commands 70, 71, 72, B0, B1, B2, D1, D2, D3 and D4 is received at step RS301 (if NO at step RS303), control proceeds from step RS303 to step RS307.

At step RS307, it is determined whether at least one of commands D0, 71 and B1 was received at step RS301. If 20 at least one of commands D0, 71 and B1 was received at step RS301 (if YES at step RS307), the sleep flag is set to "1" (step RS309), and control proceeds to step RS311. If none of commands D0, 71 and B1 was received at step RS301 (if NO at step RS307), control proceeds from step 25 RS307 to step RS311. Accordingly, when the photographing

lens 200 enters sleep mode, the rear converter 300 also enters sleep mode.

At step RS311 it is determined whether command 7F was received at step RS301. If command 7F was received 5 at step RS301 (if YES at step RS311), the rear converter data is transmitted to the body CPU 311 (step RS313) and subsequently control returns. If it is determined at step RS311 that command 7F was not received at step RS301 (if NO at step RS311), control returns. Command 7F is 10 issued for the rear converter 300. The photographing lens 200 performs the dummy data communication process (step LS449 or LS479) upon receiving command 7F issued by the body CPU 111, and the CPU 311 of the rear converter 300 sends the rear converter data to the body CPU 111 in 15 synchronization with the dummy data communication process. The body CPU 111 performs operations/processes in accordance with the rear converter data upon receiving the rear converter data.

As can be understood from the above descriptions, 20 according to the present embodiment of the SLR camera system to which the present invention is applied, a data area for the rear converter 300 is secured in advance so that the rear converter 300 can send the rear converter data to the camera body during data communication between 25 the camera body 100 and the photographing lens 200. This

makes it possible for the camera body 100 to communicate with each of the photographing lens 200 and the rear converter 300, and to control operations of the photographing lens 200 and the rear converter 300 without
5 the need for switching data communications therebetween.

The rear converter can be provided with one or more additional functions/capabilities, e.g., a variable diaphragm function and a swing/tilt function. If the rear converter is provided with such one or more
10 additional functions/capabilities, data on such additional functions/capabilities can be transmitted to the camera body as the rear converter data.

As can be understood from the foregoing, according to the present embodiment of the SLR camera system to which
15 the present invention is applied, since the rear converter includes a group of relay channels via which the first group of contacts of the camera body are electrically connected with the second group of contacts of the photographing lens, respectively, in a state where the
20 rear converter is properly mounted between the camera body and the photographing lens; a memory in which rear converter data on the rear converter is stored, the memory including at least one port electrically connected to corresponding at least one relay channel of the group of
25 relay channels; and a controller which controls a reading

operation of the rear converter data from the memory, the controller including at least one port electrically connected to corresponding at least one relay channel of the group of relay channels; wherein the memory and the 5 controller have a function to send the rear converter data to the camera body while the camera body and the photographing lens communicate with each other via the first group of contacts, the second group of contacts, and the group of relay channels; neither the camera body 10 nor the photographing lens needs to be provided with any additional contacts for data communications. In addition, data on the rear converter can be transmitted to the camera body without the need for switching in data communications between the photographing lens and the 15 rear converter.

Obviously changes may be made in the specific embodiments of the present invention described herein, such modifications being within the spirit and scope of the invention claimed. It is indicated that all matter 20 contained herein is illustrative and does not limit the scope of the present invention.